

EXAMINER'S  
SEARCH NOTES

13  
EIC 3600 COMMERCIAL DATABASE SEARCH REQUEST

☐ RUSH - SPE signature required: \_\_\_\_\_

Staff Use Only

Access DB#

Business Methods Case: 705/26, Cross 705/22,27,28,29 Log Number: \_\_\_\_\_

Write in 705 subclass(es) to search required files for 705 cases or cases cross referenced in 705.

52780

Requester's Full Name: Andrew Fischer Examiner #: 75586 Date: May 3, 2005

Art Unit: 3627 Phone Number: 305-0292 Serial Number: 09/777,057

Bldg & Room #: PK5 7B-09 Results Format Preferred: PAPER ☒ DISK ☐ E-MAIL ☐

If more than one search is submitted, please prioritize searches in order of need.

Provide the PALM Bib page or the following:

(Total Pages including this sheet: 6 )

Title of Invention: Bib Data Sheet Attached

Inventors (provide full names):

Earliest Priority Filing Date: 2/5/01

Requested attachments:

- If possible, provide the cover sheet, the IDS, examples, or relevant citations, authors, etc, if known.
- Please attach copies of the parts of this case that help explain or are most pertinent to this search. Examples are:  
**abstract, background, summary, claim(s) [not all of the claims].**

Abstract, Background of the Invention, and claim1 included.

The claimed or apparent novelty of the invention is:

Selecting switchgear product configurations and automatically generating a drawing or schematic.

This search should focus on:

(Also include keywords or synonyms)

Configuring a switchgear system.

In essence, software that helps a user design, purchase, or configure switchgear.

408

considered  
agt 11/19/05

If you have any questions or need help with keywords, please feel free to contact me.

Please include a copy of this page with the search report.

Special Instructions or Other Comments

COMPLETE INTERNET & PRIOR ART SEARCH REQUESTED

Eric  
2/6

EXAMINER'S  
SEARCH NOTES

File 256:TecInfoSource 82-2005/Mar  
(c) 2005 Info.Sources Inc.  
File 2:INSPEC 1969-2005/Apr W4  
(c) 2005 Institution of Electrical Engineers  
File 35:Dissertation Abs Online 1861-2005/Apr  
(c) 2005 ProQuest Info&Learning  
File 65:Inside Conferences 1993-2005/May W2  
(c) 2005 BLDSC all rts. reserv.  
File 99:Wilson Appl. Sci & Tech Abs 1983-2005/Apr  
(c) 2005 The HW Wilson Co.  
File 583:Gale Group Globalbase(TM) 1986-2002/Dec 13  
(c) 2002 The Gale Group  
File 474:New York Times Abs 1969-2005/May 11  
(c) 2005 The New York Times  
File 475:Wall Street Journal Abs 1973-2005/May 11  
(c) 2005 The New York Times

Set	Items	Description
S1	12485	SWITCHGEAR?
S2	19839	(SOFTWARE OR APPS OR APPLICATIONS OR PROGRAM OR PROGRAMS) (-5N) (CONFIGUR? OR SPECIFICATION? OR SPEC OR SPECS)
S3	38428	(AUTOMATIC? OR SIMULTAN? OR INSTANT? OR IMMEDIAT? OR INSTANTAN? OR ON(1W)FLY) (5N) (CREAT? OR GENERAT? OR PRODUCING OR PRODUCE? OR DEVELOP? ?)
S4	2084295	DRAWING? OR SCHEMATIC? OR DESIGN?
S5	233	AU=(LESLIE, D? OR LESLIE D? OR LAUFENBERG, R? OR LAUFENBERG R?)
S6	32311	S1 OR S2
S7	166	S6(5N)S3
S8	14	S7(5N)S4
S9	0	S5 AND S1

*considered 11/10/05*

8/5/1 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

8207156 INSPEC Abstract Number: C2005-01-6150C-033

**Title: Code generation & high-integrity embedded systems**

Author(s): Erkkinen, T.

Journal: Dr. Dobb's Journal vol.29, no.6 p.68-70

Publisher: CMP Media LLC,

Publication Date: June 2004 Country of Publication: USA

CODEN: DDJS DM ISSN: 1044-789X

SICI: 1044-789X(200406)29:6L:68:CGHI;1-V

Material Identity Number: B719-2004-005

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: Model-based design is a software development approach based on executable specification models and automatic code generators. Production-code generation provides a common framework for adding software details to the behavioral algorithm model. The author's first step through each software engineering activity, was demonstrating some of the available methods and tools for model-based design and production-code generation technologies.

Subfile: C

Descriptors: configuration management; embedded systems; formal specification; program compilers; program verification; software standards

Identifiers: production-code generation; high-integrity embedded system; model-based design; software development approach; automatic code generator ; software engineering

Class Codes: C6150C (Compilers, interpreters and other processors); C6110B (Software engineering techniques); C6110F (Formal methods)

Copyright 2004, IEE

8/5/2 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

7073835 INSPEC Abstract Number: C2001-12-7420-015

**Title: Matlab-based real-time framework for distributed control systems**

Author(s): Marcos, M.M.; Portillo, J.; Bass, J.M.

Author Affiliation: Escuela Superior de Ingenieros de Bilbao, Univ. of the Basque Country, Bilbao, Spain

Conference Title: Algorithms and Architectures for Real-Time Control 2000, (AARTC'2000). Proceedings volume from the 6th IFAC Workshop p. 199-204

Editor(s): Hernandez, V.; Irwin, G.W.

Publisher: Elsevier Science, Kidlington, UK

Publication Date: 2000 Country of Publication: UK vii+245 pp.

ISBN: 0 08 043685 4 Material Identity Number: XX-2000-01693

Conference Title: Proceedings of Workshop 6th Algorithms and Architectures for Real-Time Control

Conference Sponsor: IFAC

Conference Date: 15-17 May 2000 Conference Location: Palma de Mallorca, Spain

Language: English Document Type: Conference Paper (PA)

Treatment: Applications (A); Practical (P)

Abstract: Embedded systems are computer-based systems that often must have deterministic temporal behaviour. The application software must ensure overall system performance in terms of hard real-time constraints, reliability and safety requirements. Often application constraints include distribution of input/output. In some other cases, distribution is needed

to achieve demanding performance or dependability requirements. This paper presents the design and implementation of the so-called real time framework (RTF) that consists of a general environment for the easy generation of distributed control **software**. RTF covers the **specification** of the distributed system to **automatic** code **generation**. The **design** is based on the integration of well-known commercial software for control systems into a framework that extends its use to distributed systems using a fieldbus as communication system. The internal design includes a model of the system in each design phase and information consistency is assured through an internal database. (22 Refs)

Subfile: C

Descriptors: computerised control; distributed control; field buses; real-time systems

Identifiers: Matlab-based real-time framework; distributed control systems; fieldbus; information consistency; internal database; computer-based systems; hard real-time constraints

Class Codes: C7420 (Control engineering computing); C5610F (Instrumentation buses)

Copyright 2001, IEE

8/5/3 (Item 3 from file: 2)

DIALOG(R) File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

6306482 INSPEC Abstract Number: C1999-09-6115-010

**Title:** T-VEC/sup TM/ product summary

Conference Title: Proceedings. 2nd IEEE Workshop on Industrial Strength Formal Specification Techniques p.86-7

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 1999 Country of Publication: USA x+143 pp.

ISBN: 0 7695 0081 1 Material Identity Number: XX-1998-03191

U.S. Copyright Clearance Center Code: 0 7695 0081 1/99/\$10.00

Conference Title: Proceedings of 1998 Workshop on Industrial Strength Formal Specification Techniques

Conference Sponsor: IEEE Comput. Soc. Tech. Committee on Complexity in Comput.; Florida Atlantic Univ

Conference Date: 21-23 Oct. 1998 Conference Location: Boca Raton, FL, USA

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P); Product Review (R)

Abstract: T-VEC's mission is to be the leading supplier of software verification and testing solutions and to enable our customers to improve their software quality while reducing development time and cost. To that end, we provide a premier set of methods and tools to automate key elements of the software development life cycle, thereby eliminating many labor-intensive and error-prone tasks. Our approach is based on automated specification-based testing. The T-VEC toolset performs **automatic** test vector and test driver **generation** from **software** requirement and **design specifications**. In addition the toolset provides powerful specification analysis capabilities to ensure consistency and completeness early in the life cycle. (0 Refs)

Subfile: C

Descriptors: computer aided software engineering; program testing; program verification; software packages; software quality; software tools

Identifiers: T-VEC; software verification; software quality; development time; software development life cycle; automated specification-based testing; automatic test vector generation; test driver generation; software requirement; design specifications; specification analysis capabilities

Class Codes: C6115 (Programming support); C6110F (Formal methods)

Copyright 1999, IEE

8/5/4 (Item 4 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

5928272 INSPEC Abstract Number: C9807-7104-008

**Title: WebWork: METEOR/sub 2/'s Web-based workflow management system**

Author(s): Miller, J.A.; Palaniswami, D.; Sheth, A.P.; Kochut, R.J.; Singh, H.

Author Affiliation: Dept. of Comput. Sci., Georgia Univ., Athens, GA, USA

Journal: Journal of Intelligent Information Systems: Integrating Artificial Intelligence and Database Technologies vol.10, no.2 p. 185-215

Publisher: Kluwer Academic Publishers,

Publication Date: March-April 1998 Country of Publication: Netherlands

CODEN: JIISEH ISSN: 0925-9902

SICI: 0925-9902(199803/04)10:2L:185:WMBW;1-O

Material Identity Number: C318-98002

U.S. Copyright Clearance Center Code: 0925-9902/98/\$9.50

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: METEOR/sub 2/ workflow management systems consist of both: (1) design/build time and (2) runtime/enactment components for implementing workflow applications. An enactment system provides the command, communication and control for the individual tasks in the workflow. Tasks are the run time instances of intra-or inter-enterprise applications. We are developing three implementations of the METEOR/sub 2/ model: WebWork, OrbWork and NeoWork. The paper discusses WebWork, an implementation relying solely on Web technology as the infrastructure for the enactment system. WebWork supports a distributed implementation with participation of multiple Web servers. It also supports **automatic code generation of workflow applications from design specifications** produced by a comprehensive graphical **designer**. WebWork has been developed as a complement of its more heavy weight counterparts (OrbWork and NeoWork), with the goal of providing ease of workflow application development, installation, use and maintenance. WebWork has been installed by several of the LSDIS Lab's industrial partners for testing, evaluation and building workflow applications. (35 Refs)

Subfile: C

Descriptors: application generators; formal specification; Internet; office automation

Identifiers: WebWork; Web based workflow management system; METEOR/sub 2/ workflow management systems; runtime/enactment components; workflow applications; enactment system; run time instances; METEOR/sub 2/ model; OrbWork; NeoWork; distributed implementation; multiple Web servers; automatic code generation; design specifications; graphical designer; workflow application development

Class Codes: C7104 (Office automation); C6150N (Distributed systems software); C7210 (Information services and centres); C6115 (Programming support); C6110F (Formal methods); C6180 (User interfaces)

Copyright 1998, IEE

8/5/5 (Item 5 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

5106898 INSPEC Abstract Number: A9524-2844-026, B9512-8220B-050, C9512-3340F-011

**Title: Design, verification and validation of digital safety systems for**

Sylvia Keys

12-May-05 12:42 PM

## Temelin

Author(s): Waclo, J.

Author Affiliation: Westinghouse Electr. Corp., Pittsburgh, PA, USA

Conference Title: Advanced Control and Instrumentation Systems in Nuclear Power Plants. Design, Verification and Validation. IAEA/IWG/ATWR & NPPCI Technical Committee Meeting (VTT-SYMP-147) p.235-50

Editor(s): Haapanen, P.

Publisher: Tech. Res. Centre of Finland, Espoo, Finland

Publication Date: 1995 Country of Publication: Finland 578 pp.

Conference Title: Advanced Control and Instrumentation Systems in Nuclear Power Plants. Design, Verification and Validation. IAEA/IWG/ATWR & NPPCI Technical Committee Meeting (VTT-SYMP-147)

Conference Date: 20-23 June 1994 Conference Location: Espoo, Finland

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P)

Abstract: The design process activities involved in the successful implementation of digital systems to today's Nuclear I&C Safety Systems are not achieved through happenstance. Within Westinghouse there has been a longstanding program to utilize digital state-of-the-art technology for protection system advancement. This paper describes the evolution of the Westinghouse design process for digital safety related systems with an overall process optimization goal to produce systems of the highest possible quality within a real-world framework of resource availability, project schedules and regulatory policies customary to the nuclear industry. This paper focuses on the design process of the digital safety systems to be installed in the Czech Republic at the Temelin site. These digital I&C systems are being implemented as an upgrade to the VVER class of Soviet designed reactors. The Temelin case study reviews the **design** process as it relates to **software design**, verification and validation, **configuration** management, **automatic code generation**, testing, industry standards and licensing. (0 Refs)

Subfile: A B C

Descriptors: configuration management; digital control; fission reactor core control; fission reactor instrumentation; fission reactor safety; nuclear engineering computing; program verification; protection; safety systems

Identifiers: digital safety systems; Temelin; software design; software verification; instrumentation system; control system; digital state-of-the-art technology; protection system; Westinghouse design process; Czech Republic; VVER class reactors; project schedules; resource availability; software validation; configuration management; automatic code generation; testing; industry standards; licensing

Class Codes: A2844 (Fission reactor protection systems, safety and accidents); A2843D (Core control and guidance in fission reactors); A2843H (Instrumentation and experiments with fission reactors); A2850G (Light water reactors); B8220B (Nuclear reactors); B0160 (Plant engineering, maintenance and safety); C3340F (Control of nuclear systems); C7470 (Nuclear engineering computing); C7420 (Control engineering computing)

Copyright 1995, IEE

8/5/6 (Item 6 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

4891869 INSPEC Abstract Number: C9504-6110-013

Title: IEE Colloquium on 'Partitioning in Hardware-Software Codesigns' (Digest No.1995/032)

Publisher: IEE, London, UK

Publication Date: 1995 Country of Publication: UK 64 pp.

Conference Title: IEE Colloquium on 'Partitioning in Hardware-Software

Codesigns' (Digest No.1995/032)

Conference Sponsor: IEE

Conference Date: 13 Feb. 1995 Conference Location: London, UK

Language: English Document Type: Conference Proceedings (CP)

Treatment: Practical (P)

Abstract: The following topics were dealt with: hardware/software partitioning for performance enhancement; design of hybrid hardware/software systems with INSYDE methodology; an automated approach to hardware- **software** codesign; real-time system; DisCo **specifications**; Petri net based codesign system; **automatic** code **generation**; closed loop controller ASIC **design**; and fingerprint recognition system design.

Subfile: C

Descriptors: computer architecture; formal specification; systems analysis; systems engineering

Identifiers: hardware software partitioning; systems design; performance enhancement; INSYDE methodology; hardware-software codesign; real-time system; DisCo specifications; Petri net based codesign system; automatic code generation; closed loop controller ASIC design; fingerprint recognition system design

Class Codes: C6110 (Systems analysis and programming); C5220 (Computer architecture)

Copyright 1995, IEE

8/5/7 (Item 7 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

04285527 INSPEC Abstract Number: C9301-6115-004

Title: **A new programming environment with module abstracting and refining using PAD**

Author(s): Hara, K.; Yang Yi

Author Affiliation: Dept. of Mech. Eng., Doshisha Univ., Kyoto, Japan

Journal: Science and Engineering Review of Doshisha University vol.33, no.2 p.120-31

Publication Date: July 1992 Country of Publication: Japan

CODEN: DDRKAZ ISSN: 0036-8172

Language: Japanese Document Type: Journal Paper (JP)

Treatment: New Developments (N); Practical (P)

Abstract: Describes a new CASE tool, in which program analysis diagram (PAD) is used to help users refine their software specifications phase by phase. Software documents, such as module hierarchical structure diagram, and **program specifications** are **generated automatically**. The tool is **designed** for UNIX, and is composed of multi-file structured editor, C language translator, document generator, inverse PAD translator, and integrated graphics user interface with windows. The user's programs, both new and existing ones, can be expressed in hierarchical PAD according to the principle of structured programming. Documentation is in close connection with software design since program source code can also be generated automatically, thus, the consistency between program and document is ensured. Software re-use and information hiding based on a block conception can also be implemented. By keeping keyboard input information in a specific file, a program can be recovered to any past version. (8 Refs)

Subfile: C

Descriptors: graphical user interfaces; programming environments; software reusability; software tools; structured programming; text editing

Identifiers: module refinement; software reuse; programming environment; module abstracting; CASE tool; program analysis diagram; software specifications; module hierarchical structure diagram; multi-file structured editor; C language translator; document generator; inverse PAD

Sylvia Keys

12-May-05 12:42 PM

translator; graphics user interface; windows; structured programming;  
software design; information hiding; block conception; keyboard input  
information

Class Codes: C6115 (Programming support)

8/5/8 (Item 8 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

04152395 INSPEC Abstract Number: C9206-6110B-068

**Title: Software testing**

Author(s): Kawata, H.; Yoshida, H.; Nagai, M.; Saijo, H.

Author Affiliation: Fujitsu Ltd., Shizuoka, Japan

Journal: Journal of Information Processing vol.14, no.3 p.246-53

Publication Date: 1991 Country of Publication: Japan

CODEN: JIPRDE ISSN: 0387-6101

Language: English Document Type: Journal Paper (JP)

Treatment: Applications (A); Practical (P)

Abstract: This paper briefly surveys the status of software testing and provides an introduction to the methodologies and techniques employed by Fujitsu Limited. The survey examines software testing from the following viewpoints: **designing** test cases from external **specifications**; **automatic** test **program** **generation**; fault injection testing; and application system testing. (23 Refs)

Subfile: C

Descriptors: formal specification; program testing

Identifiers: software testing; specifications; automatic test program generation; fault injection testing; application system testing

Class Codes: C6110B (Software engineering techniques)

8/5/9 (Item 9 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

04001682 INSPEC Abstract Number: C91074380

**Title: KMDS: an expert system for integrated hardware/software design of microprocessor-based digital systems**

Author(s): Yau-Hwang Kuo; Ling-Yang Kung; Ching-Chung Tzeng; Guang-Huei Jeng; Wei-Kuo Chia

Author Affiliation: Nat. Cheng Kung Univ., Tainan, Taiwan

Journal: IEEE Micro vol.11, no.4 p.32-5, 86-92

Publication Date: Aug. 1991 Country of Publication: USA

CODEN: IEMIDZ ISSN: 0272-1732

U.S. Copyright Clearance Center Code: 0272-1732/91/0800-0032\$01.00

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: A design automation system called KMDS, which makes use of a knowledge-based expert system and external algorithmic procedures to realize a modularized and flexible design automation tool, is presented. Its system configuration and characteristics, knowledge base and device libraries, top-down design methodology, and external utilities are described. Besides aiding the design of single-board microcomputers, KMDS helps designers of intelligence interface cards such as color display adapters and printer server cards. KMDS is flexible enough to incorporate various types of knowledge into the system and to overcome the problems that result from the existence of a large number of candidate solutions under a very high-level **design specification**. KMDS **automatically generates** a control **program**, making the fully automatic **design** of



digital systems possible. (10 Refs)

Subfile: C

Descriptors: CAD; digital systems; electronic engineering computing; expert systems; knowledge based systems

Identifiers: microprocessor-based digital systems; design automation system; KMDS; knowledge-based expert system; external algorithmic procedures; design automation tool; device libraries; top-down design; external utilities; single-board microcomputers; intelligence interface cards; color display adapters; printer server cards; control program

Class Codes: C7410D (Electronic engineering); C5400 (Analogue and digital computers and systems); C6170 (Expert systems)

8/5/10 (Item 10 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

03814292 INSPEC Abstract Number: C91017316

**Title: Representation of object-oriented data models**

Author(s): Shuguang Hong; Maryanski, F.

Author Affiliation: Ontologic Inc., Burlington, MA, USA

Journal: Information Sciences vol.52, no.3 p.247-84

Publication Date: 1990 Country of Publication: USA

CODEN: ISIJBC ISSN: 0020-0255

U.S. Copyright Clearance Center Code: 0020-0255/90/\$03.50

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: Database design tools have been introduced as a means of reducing the expertise required of the designer, to free him or her from the implementation details, and to aid in managing the complexity of the design task. However, one of the common shortcomings of database design tools is that they are closely tied to specific data models, thus limiting the applicability of their underlaying concepts and restricting the reusability of the design software. SeaWeed, a component of the Data Model Compiler (DMC) project, proposes a solution by **automatically generating database design software** from data model **specifications**. In this paradigm, a data model specification model, or metamodel of data models, is introduced for specifying particular object-oriented data models. Using the knowledge in the **specification**, conceptual database **design software** is **automatically generated** for the given data model. This paper presents a metamodel for object-oriented data models and offers a technique for expressing the semantics of an object-oriented data model in terms of the metaobject types, which consist of attributes, operations, constraints, and graphical representations. (38 Refs)

Subfile: C

Descriptors: formal specification; object-oriented databases

Identifiers: database design tools; representation; object-oriented data models; complexity; SeaWeed; data model specification model; metamodel of data models; attributes; operations; constraints; graphical representations

Class Codes: C6160Z (Other DBMS); C6110 (Systems analysis and programming)

8/5/11 (Item 11 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

03706292 INSPEC Abstract Number: C90057782

**Title: Automatic programming by design specification database for communication software**

Author(s): Makuta, K.; Aoe, H.; Atsumi, T.; Yamamoto, T.  
Author Affiliation: Totsuka Works Hitachi Ltd., Japan  
Conference Title: GLOBECOM '89. IEEE Global Telecommunications Conference and Exhibition. Communications Technology for the 1990s and Beyond (Cat. No.89CH2682-3) p.480-6 vol.1

Publisher: IEEE, New York, NY, USA

Publication Date: 1989 Country of Publication: USA 3 vol. xxxii+1975 pp.

U.S. Copyright Clearance Center Code: CH2682-3/89/0000-0480\$01.00

Conference Sponsor: IEEE

Conference Date: 27-30 Nov. 1989 Conference Location: Dallas, TX, USA

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

**Abstract:** **Automatic program generation** by a **design specification** database is discussed. It is noted that, in the area of communication software, modular design techniques and high-level language compilers and debuggers have been introduced, but few tools for supporting the software design process have been implemented. It is proposed that the design specifications be standardized and each design specification be put in a database of relational table forms for the purpose of supporting the design process. The types of design specifications needed to be incorporated in the design specification database and the elements of each table format are discussed. On the basis of these considerations, a design specification database support system was developed. This support system includes a feature for automatically generating program codes of the module interface, a document printing feature, and a table editor of the design specification database. (2 Refs)

Subfile: C

Descriptors: automatic programming; network operating systems; relational databases.

Identifiers: automatic program generating; database access language; design process support; design specification database; communication software; relational table forms; module interface; document printing; table editor

Class Codes: C6115 (Programming support); C6150J (Operating systems); C6160D (Relational DBMS)

8/5/12 (Item 12 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2005 Institution of Electrical Engineers. All rts. reserv.

03142686 INSPEC Abstract Number: C88035332

**Title:** YPS: YAC II programming system

Author(s): Mikami, J.; Higashimura, A.

Journal: Fujitsu vol.39, no.1 p.29-35

Publication Date: 1988 Country of Publication: Japan

CODEN: FUJTAR ISSN: 0016-2515

Language: Japanese Document Type: Journal Paper (JP)

Treatment: Practical (P)

**Abstract:** Fujitsu has developed a tool for software development support that allows users to create program specifications by using special expressions of graphic and native language notation to enable **automatic program generation** from these **specifications**. YPS is **designed** to increase reliability and improve productivity in program development by introducing new expressions based on the human thought process, as opposed to conventional programming that uses symbolic language. The authors identify program development problems, explain the functional features designed to resolve these problems, and describe the effectiveness of this tool. (5 Refs)

Subfile: C

Descriptors: automatic programming; software engineering; software tools  
Identifiers: software development tool; graphic language notation; YPS;  
YAC II; Fujitsu; program specifications; native language notation;  
automatic program generation; reliability; human thought process  
Class Codes: C6110B (Software engineering techniques); C6115 (Programming support)

8/5/13 (Item 13 from file: 2)  
DIALOG(R) File 2:INSPEC  
(c) 2005 Institution of Electrical Engineers. All rts. reserv.

02852812 INSPEC Abstract Number: C87023919

**Title: Development and evaluation of program design specification language SL**

Author(s): Inada, M.; Okamoto, T.; Watanabe, S.; Nakamura, Y.  
Author Affiliation: Software Production Technol. Lab., NTT Corp., Tokyo, Japan

Journal: Transactions of the Information Processing Society of Japan  
vol.27, no.6 p.601-11

Publication Date: 1986 Country of Publication: Japan

CODEN: JSGRD5 ISSN: 0387-5806

Language: Japanese Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: Describes SL (Superb Data Oriented Language), a program design specification language which inputs program design specifications and automatically generates a COBOL program. SL, designed to materialize a program generator which is widely applicable for large-sized programs, is characterized by the following: (1) The program is automatically generated from specifications designed by using the language, which is standardized by developing a descriptive method of the design specifications based on a method of data structure design so that the database can be also dealt with. (2) The objective program code corresponding to program input/output processing and special processing of business-use functions, etc. can be defined by users. (3) The program design specifications can be described in various ways. (10 Refs)

Subfile: C

Descriptors: automatic programming; data structures; specification languages

Identifiers: automatic programming; program design specification language; SL; Superb Data Oriented Language; COBOL program; program generator; large-sized programs; descriptive method; data structure design; program code; program input/output processing; business-use functions; program design specifications

Class Codes: C6115 (Programming support); C6140D (High level languages)

8/5/14 (Item 14 from file: 2)  
DIALOG(R) File 2:INSPEC  
(c) 2005 Institution of Electrical Engineers. All rts. reserv.

02030384 INSPEC Abstract Number: C83018077

**Title: ADD: an automated tool for program design and documentation**

Author(s): Ting, T.C.

Author Affiliation: Worcester Polytech. Inst., Worcester, MA, USA

Conference Title: NBS FIPS Software Documentation. Proceedings of a Workshop (NBS-SP-500-94) p.95-109

Editor(s): Neumann, A.J.

Publisher: NBS, Washington, DC, USA

Publication Date: 1982 Country of Publication: USA x+283 pp.

Conference Date: 3 March 1982      Conference Location: Gaithersburg, MD, USA

Language: English      Document Type: Conference Paper (PA)

Treatment: Practical (P)

Abstract: An approach which integrates the activities of software design and documentation is proposed, described, and discussed. An automated tool called ADD which uses a data dictionary system is suggested to support this approach. The unified approach not only offers solutions to some of the important documentation problems, but it provides a structured means for better program design and coding. Program design process is enhanced and guided by a structured design template. Program design documents are generated automatically to serve as 'blueprints' for programming. The use of a 'program coding template' provides a structure for coding. Program module interface conditions are **automatically generated** and controlled from the **design specifications**. Program modules are tested by using the predesigned and stored test data to certify their correctness. The structure of the tool is illustrated. How the automated tool may be used and the benefits of such an automated tool are discussed. (19 Refs)

Subfile: C

Descriptors: database management systems; program and system documentation; program testing; software engineering

Identifiers: program testing; DBMS; software engineering; ADD; automated tool; program design; documentation; software design; data dictionary system; structured design template; module interface; stored test data; correctness

Class Codes: C0310F (Software development management); C6110 (Systems analysis and programming); C6160 (Database management systems (DBMS))

?

EXAMINER'S  
SEARCH NOTES

*See #ed  
articles  
hevin  
ggt 11/19/05*

File 16:Gale Group PROMT(R) 1990-2005/May 11  
(c) 2005 The Gale Group  
File 148:Gale Group Trade & Industry DB 1976-2005/May 12  
(c) 2005 The Gale Group  
File 160:Gale Group PROMT(R) 1972-1989  
(c) 1999 The Gale Group  
File 275:Gale Group Computer DB(TM) 1983-2005/May 12  
(c) 2005 The Gale Group  
File 621:Gale Group New Prod.Annou.(R) 1985-2005/May 12  
(c) 2005 The Gale Group  
File 636:Gale Group Newsletter DB(TM) 1987-2005/May 12  
(c) 2005 The Gale Group  
File 9:Business & Industry(R) Jul/1994-2005/May 11  
(c) 2005 The Gale Group  
File 15:ABI/Inform(R) 1971-2005/May 12  
(c) 2005 ProQuest Info&Learning  
File 20:Dialog Global Reporter 1997-2005/May 12  
(c) 2005 The Dialog Corp.  
File 95:TEME-Technology & Management 1989-2005/Apr W1  
(c) 2005 FIZ TECHNIK  
File 476:Financial Times Fulltext 1982-2005/May 12  
(c) 2005 Financial Times Ltd  
File 610:Business Wire 1999-2005/May 11  
(c) 2005 Business Wire.  
File 613:PR Newswire 1999-2005/May 12  
(c) 2005 PR Newswire Association Inc  
File 624:McGraw-Hill Publications 1985-2005/May 11  
(c) 2005 McGraw-Hill Co. Inc  
File 634:San Jose Mercury Jun 1985-2005/May 11  
(c) 2005 San Jose Mercury News  
File 810:Business Wire 1986-1999/Feb 28  
(c) 1999 Business Wire  
File 813:PR Newswire 1987-1999/Apr 30  
(c) 1999 PR Newswire Association Inc  
File 47:Gale Group Magazine DB(TM) 1959-2005/May 12  
(c) 2005 The Gale group  
File 635:Business Dateline(R) 1985-2005/May 12  
(c) 2005 ProQuest Info&Learning  
File 570:Gale Group MARS(R) 1984-2005/May 11  
(c) 2005 The Gale Group  
File 477:Irish Times 1999-2005/May 12  
(c) 2005 Irish Times  
File 710:Times/Sun.Times(London) Jun 1988-2005/May 11  
(c) 2005 Times Newspapers  
File 711:Independent(London) Sep 1988-2005/May 11  
(c) 2005 Newspaper Publ. PLC  
File 756:Daily/Sunday Telegraph 2000-2005/May 12  
(c) 2005 Telegraph Group  
File 757:Mirror Publications/Independent Newspapers 2000-2005/May 12  
(c) 2005  
File 387:The Denver Post 1994-2005/May 11  
(c) 2005 Denver Post  
File 471:New York Times Fulltext 1980-2005/May 12  
(c) 2005 The New York Times  
File 492:Arizona Repub/Phoenix Gaz 1986-2002/Jan 06  
(c) 2002 Phoenix Newspapers  
File 494:St LouisPost-Dispatch 1988-2005/May 09  
(c) 2005 St Louis Post-Dispatch  
File 498:Detroit Free Press 1987-2005/Mar 31  
(c) 2005 Detroit Free Press Inc.  
File 631:Boston Globe 1980-2005/May 11

(c) 2005 Boston Globe  
 File 633:Phil.Inquirer 1983-2005/May 10  
 (c) 2005 Philadelphia Newspapers Inc  
 File 638:Newsday/New York Newsday 1987-2005/May 11  
 (c) 2005 Newsday Inc.  
 File 640:San Francisco Chronicle 1988-2005/May 12  
 (c) 2005 Chronicle Publ. Co.  
 File 641:Rocky Mountain News Jun 1989-2005/May 11  
 (c) 2005 Scripps Howard News  
 File 702:Miami Herald 1983-2005/May 07  
 (c) 2005 The Miami Herald Publishing Co.  
 File 703:USA Today 1989-2005/May 10  
 (c) 2005 USA Today  
 File 704:(Portland)The Oregonian 1989-2005/May 11  
 (c) 2005 The Oregonian  
 File 713:Atlanta J/Const. 1989-2005/May 12  
 (c) 2005 Atlanta Newspapers  
 File 714:(Baltimore) The Sun 1990-2005/May 12  
 (c) 2005 Baltimore Sun  
 File 715:Christian Sci.Mon. 1989-2005/May 12  
 (c) 2005 Christian Science Monitor  
 File 725:(Cleveland)Plain Dealer Aug 1991-2005/May 10  
 (c) 2005 The Plain Dealer  
 File 735:St. Petersburg Times 1989- 2005/May 11  
 (c) 2005 St. Petersburg Times

Set	Items	Description
S1	23021	SWITCHGEAR?
S2	229203	(SOFTWARE OR APPS OR APPLICATIONS OR PROGRAM OR PROGRAMS) (-5N) (CONFIGUR? OR SPECIFICATION? OR SPEC OR SPECS)
S3	321156	(AUTOMATIC? OR SIMULTAN? OR INSTANT? OR IMMEDIAT? OR INSTANTAN? OR ON(1W)FLY) (5N) (CREAT? OR GENERAT? OR PRODUCING OR PRODUCE? OR DEVELOP? ?)
S4	15891166	DRAWING? OR SCHEMATIC? OR DESIGN?
S5	118	AU=(LESLIE, D? OR LESLIE D? OR LAUFENBERG, R? OR LAUFENBERG R?)
S6	252012	S1 OR S2
S7	495	S6(5N)S3
S8	64	S7(5N)S4
S9	54	S8 NOT PY>2001
S10	39	RD (unique items)
S11	0	S5 AND S1

*considered off 11/10/05*

*See \* for possible good art.*

10/3,K/1 (Item 1 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

07174690 Supplier Number: 60899409 (USE FORMAT 7 FOR FULLTEXT)  
**Bricsnet Enters the Online Project-Site Fray. (Company Business and Marketing)**  
HILL, HEIDI  
Cadence, v15, n3, p13  
March, 2000  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 806

... on the industry standard DWG file format. Built on the ACIS modeling kernel, Bricsnet architectural **design software** can **automatically create** accurate quantities, **specifications** and engineering **drawings**. Users and developers can create intelligent building components with Visual Basic for Applications that can...

10/3,K/2 (Item 2 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

04481427 Supplier Number: 46578800 (USE FORMAT 7 FOR FULLTEXT)  
**Standards sharpen fuzzy methodology**  
Electronic Engineering Times, p64  
July 29, 1996  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 1573

... vocabulary the fuzzy rules use to express the strategy. State-of-the-art fuzzy-logic **software** -development tools automate the **specification** of linguistic variables and **automatically generate** documentation of the **design** process. For example, fuzzyTECH features the Fuzzy Variables Wizard, which creates complete definition of linguistic...

10/3,K/3 (Item 3 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

04099307 Supplier Number: 45975400 (USE FORMAT 7 FOR FULLTEXT)  
**Drawings in a Snap**  
Computer-Aided Engineering, p20  
Dec 1, 1995  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Academic Trade  
Word Count: 191

(USE FORMAT 7 FOR FULLTEXT)  
TEXT:  
Synthesis MKBS automated drafting and **design software** allows users to **create** custom, **configured** CAD manufacturing **drawings automatically**. The **software** - compatible with AutoCAD Release 12 and 13, for both DOS and Windows - does not require...

10/3,K/4 (Item 4 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

04009143 Supplier Number: 45823630 (USE FORMAT 7 FOR FULLTEXT)

**Fast Drawings**

Computer-Aided Engineering, p52  
Oct, 1995  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Academic Trade  
Word Count: 204

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

...drafting and configuration software. Compatible with AutoCAD R12 and R13 for DOS and Windows, the **program** allows users to **create** custom-**configured**, CAD-manufactured **drawings** **automatically**.

10/3,K/5 (Item 5 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

03406247 Supplier Number: 44736641 (USE FORMAT 7 FOR FULLTEXT)

**FOUNDATIONR ANNOUNCES FIRST TOOL TO SUPPORT HUNDREDS OF CONCURRENT**

**CLIENT/SERVER SYSTEMS DEVELOPERS**

News Release, pN/A  
June 6, 1994  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 1117

... FCP Version 2.0-LP Windows Client Option enables users of the new product to **automatically generate** enterprise-wide client/server **applications** -- from the same **design specifications** -- for both the Windows Version 3.1 and OS/2R Presentation ManagerR(PM) environments. At...

10/3,K/6 (Item 6 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

03130306 Supplier Number: 44271170 (USE FORMAT 7 FOR FULLTEXT)

**SENSOR MARKETS AND TECHNOLOGIES UPDATE: HONEYWELL IBCS CONCEPT TO ADVANCE MANUFACTURING AUTOMATION AND CONTROL**

Sensor Business Digest, v3, n>3, pN/A  
Dec, 1993  
Language: English Record Type: Fulltext  
Document Type: Newsletter; Trade  
Word Count: 1652

... is derived from a factory-wide model.

The proposed IBCS control software environment consists of **software** development tools that facilitate the **configuration**, **design**, analysis, and **automatic software** code **generation** of sophisticated real-time control systems. The concept system's Batch Production Planning and Control ...



10/3,K/7 (Item 7 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

02709437 Supplier Number: 43621496 (USE FORMAT 7 FOR FULLTEXT)  
**ANDERSEN CONSULTING'S FOUNDATIONR CLIENT/SERVER CASE TOOL NOW SUPPORTS  
CREATION OF WINDOWS CLIENTS**  
News Release, pl  
Feb 1, 1993  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 866

... total of more than  
15 million desktop PCs.

With FCP Version 1.2, users can **automatically generate** enterprise-  
wide client/server **applications** -- from the same **design  
specifications** --for both the Windows Version 3.1 and OS/2 (R)  
Presentation Manager" environments. At...

10/3,K/8 (Item 8 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

02709436 Supplier Number: 43621495 (USE FORMAT 7 FOR FULLTEXT)  
**ANDERSEN CONSULTING'S FOUNDATION (R) CLIENT/SERVER CASE TOOL NOW SUPPORTS  
CREATION OF WINDOWS CLIENTS**  
News Release, pl  
Feb 1, 1993  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 870

... total of more than  
15 million desktop PCs.

With FCP version 1.2, users can **automatically generate** enterprise-  
wide client/server **applications** -- from the same **design  
specifications** -- for both the Windows Version 3.1 and OS/2 (R)  
Presentation Manager (R) environments...

10/3,K/9 (Item 9 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

01680782 Supplier Number: 42089186 (USE FORMAT 7 FOR FULLTEXT)  
**Xilinx, Actel offer FPGA migration**  
Electronic Engineering Times, pl  
May 20, 1991  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 1311

... that's a simpler task than specifying a gate-array conversion,  
Farrell said.

The converted **designs** are derived from the FPGA **configuration program**. Then the metal mask **configuration** and test **programs** are **automatically generated**. The customer gets 100-percent-tested, pin-equivalent devices. Additional design effort is not eliminated...

10/3,K/10 (Item 10 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

01133387 Supplier Number: 41281811 (USE FORMAT 7 FOR FULLTEXT)

**FPGAs for the military shine despite shrinking DOD budget**

Electronic Engineering Times, p62

April 16, 1990

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 1488

... Like a microprocessor peripheral, the chip is configured at powerup, either by a microprocessor or **automatically** from system PROM. The **designer develops** the **configuration software** using standard macro libraries and popular CAE development tools. The FPGA is a standard product...

10/3,K/11 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

11558874 SUPPLIER NUMBER: 58036290 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**LOW-COST TECHNIQUES bring Internet connectivity to embedded devices. (Scenix Semiconductor's SX-Stack and Connect One's iChip) (Product Information)**

Nath, NS Manju

EDN, 44, 23, 159

Nov 11, 1999

ISSN: 0012-7515 LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 2799 LINE COUNT: 00235

... are available in Verilog HDL, and you may choose the protocols to include in your **design**. A **configuration program** then **automatically generates** the RTL code to **produce** the Internet system on a chip.

The i-1000 evaluation kit is also available. It...

10/3,K/12 (Item 2 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

08888122 SUPPLIER NUMBER: 18554730

**Standards sharpen fuzzy methodology. (Inform Software's fuzzyTECH fuzzy-logic development software) (includes a related article on benchmarking the fuzzy-logic process) (Special Report on Embedded Systems) (Product Information)**

von Altrock, Constantin

Electronic Engineering Times, n912, p64(3)

July 29, 1996

ISSN: 0192-1541 LANGUAGE: English RECORD TYPE: Fulltext; Abstract

WORD COUNT: 1729 LINE COUNT: 00148

... vocabulary the fuzzy rules use to express the strategy.

State-of-the-art fuzzy-logic **software** -development tools automate the **specification** of linguistic variables and **automatically generate** documentation of the **design** process. For example, fuzzyTECH features the Fuzzy Variables Wizard, which creates complete definition of linguistic...

10/3,K/13 (Item 3 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

07201345 SUPPLIER NUMBER: 15044059 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Automatic database generation by novice end-users using English sentences.**  
(user-friendly computer-aided software engineering tool, **THE Analyst**)  
Steinberg, Geoffrey; Faley, Robert; Chinn, Susan  
Journal of Systems Management, v45, n3, p10(6)  
March, 1994  
ISSN: 0022-4839 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2898 LINE COUNT: 00239

... of system requirements being misunderstood or corrupted as they pass from user to analyst to **designer** to programmer to tester.

Thus, **software** that is **automatically created** from **specifications** is more likely to be free of human error than software developed using conventional means...

10/3,K/14 (Item 4 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

06212066 SUPPLIER NUMBER: 13618741 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Designing in 3-D. (Alias Research Inc.'s 3-D design software)**  
Babyak, Richard J.  
Appliance Manufacturer, v40, n10, p77(2)  
Oct, 1992  
ISSN: 0003-679X LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 578 LINE COUNT: 00049

... photo-realistic, 3-D images eliminates unnecessary hand drawing and model building. More importantly, the **program simultaneously creates** a **specification** database as the product is **designed** on screen.

That data can be passed directly into CAD/CAM programs (and back), allowing...

10/3,K/15 (Item 5 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

04760944 SUPPLIER NUMBER: 08594662 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**CASE tools to play a larger role in IS organizations. (computer-aided software engineering, information systems)(Applied Intelligence) (column)**  
Martin, James  
PC Week, v7, n26, p45(1)  
July 2, 1990  
DOCUMENT TYPE: column ISSN: 0740-1604 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 729 LINE COUNT: 00060

...ABSTRACT: position as information systems (IS) organizations retool for

the 1990's. I-CASE tools can **automatically generate program** code from high-level **design specifications**, an area that was formerly coded by hand using such applications as COBOL. Non-integrated...

10/3,K/16 (Item 6 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

04101813 SUPPLIER NUMBER: 07735570 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**IBM Repository will be critical to development in the '90s.**

Martin, James

PC Week, v6, n39, p84(1)

Oct 2, 1989

ISSN: 0740-1604

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 1231 LINE COUNT: 00104

... store design specifications in a standard format. Tools that obey this standard format can share **design** information.

-- An **applications** generator that extracts **design specifications** from the Repository and **automatically generates** application code. IBM's applications generator for SAA is the Cross System Product (CSP), which...

10/3,K/17 (Item 1 from file: 160)

DIALOG(R)File 160:Gale Group PROMT(R)  
(c) 1999 The Gale Group. All rts. reserv.

02243124

**ORACLE'S NEW CASE\*GENERATOR EXPANDS CASE TOOLSET**

News Release May 30, 1989 p. 1

...These two products, along with Oracle's CASE\*Method, were introduced in September 1988. CASE\* **Generator** provides the ability to **automatically generate** advanced, portable **applications** directly from **design specifications**. In this first release, CASE\*Generator receives definitions about an application's database tables and...

10/3,K/18 (Item 2 from file: 160)

DIALOG(R)File 160:Gale Group PROMT(R)  
(c) 1999 The Gale Group. All rts. reserv.

00853150

**Computer-aided design systems have evolved from simple drafting to database applications, according to GR Jackson of Bechtel Civil & Minerals.**

Chemical Engineering Progress December, 1982 p. 33-361

... lengths and back annotations; transmit drawings by data communications; automate budget and schedule updates; and **create specification-driven design programs** with **automatic** constraints for error prevention. Article examines in-house software development and adding intelligence to graphic...

10/3,K/19 (Item 3 from file: 160)

DIALOG(R)File 160:Gale Group PROMT(R)  
(c) 1999 The Gale Group. All rts. reserv.

00619439

The engine generator set business is tough competitively, yet excellent if you know how to build into the complete product some of your own componentry, according to WW Pearsall, pres of Euclid Equipment. Diesel & Gas Turbine Progress December, 1980 p. 48,491

...from Euclid's viewpoint with production primarily in sets 250 kW and below. Euclid can design, fabricate and deliver custom-designed automatic generator control switchgear in less than 8 wk. The total impact of the company's new facility and...

10/3,K/20 (Item 1 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2005 The Gale Group. All rts. reserv.

01633549 SUPPLIER NUMBER: 15012498 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
The quest for CASE. (Bank of Montreal; computer-aided software engineering)  
Blackwell, Gerry  
I.T. Magazine, v25, n11, p22(6)  
Nov, 1993  
LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2937 LINE COUNT: 00229

... the application must do. Most vendors were more interested in selling lower CASE tools for automatically generating program code from design specifications.

"We weren't looking for code generators," Mr. Hanspal says of that period in the...

10/3,K/21 (Item 2 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2005 The Gale Group. All rts. reserv.

01450323 SUPPLIER NUMBER: 11320829 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
Impacts of life cycle models on software configuration management.  
(technical)  
Bersoff, Edward H.; Davis, Alan M.  
Communications of the ACM, v34, n8, p104(15)  
August, 1991  
DOCUMENT TYPE: technical ISSN: 0001-0782 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 7504 LINE COUNT: 00624

... this time, and the resulting software tends not to satisfy performance constraints.

\* Users and/or software developers write a formal requirements specification, and a software tool is used to automatically generate the high-level design; then software engineers complete development by more conventional means. As in the first case, this...

10/3,K/22 (Item 3 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2005 The Gale Group. All rts. reserv.

01358111 SUPPLIER NUMBER: 08218966 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
Tools to manage change needed in changing times; to obtain "optimum resource utilization" developers need better configuration tools.

Bochenski, Barbara  
Software Magazine, v10, n3, p37(7)  
March, 1990  
ISSN: 0897-8085      LANGUAGE: ENGLISH      RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 3790      LINE COUNT: 00321

... With the increasing use of code generators, this is an important point. When code is **automatically generated** from **design specifications**, modifications to the end **program** will be specified by changing the design specifications rather than directly altering source code programs...

10/3,K/23      (Item 1 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2005 The Gale Group. All rts. reserv.

01071381      Supplier Number: 40361463 (USE FORMAT 7 FOR FULLTEXT)  
**FIRST COMMERCIAL PRODUCT DEVELOPED WITH DIGITALK'S SMALLTALK/V 286 RELEASED BY COMPUTER SCIENCES CORP.**  
News Release, p1  
April 20, 1988  
Language: English      Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 723

... in other CASE products currently on the market, according to Woodard. The system automates the **software specification** process, analyses **specifications** for completeness and **generates designs automatically** from the specifications.

The Smalltalk/V 286 object-oriented environment of the Design Generator provides...

10/3,K/24      (Item 2 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2005 The Gale Group. All rts. reserv.

01003131      Supplier Number: 39524070 (USE FORMAT 7 FOR FULLTEXT)  
**ADR/DL1 TRANSPARENCY ELIMINATES DATA BASE REDESIGN Relational Data Base Design Subsystem is Key**  
PR Newswire, pN/A  
May 9, 1985  
Language: English      Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 265

... s Relational Data Base Design Subsystem reads the DL/1 Data Base Definition (DBD) and **Program Specification Block (PSB)** source statements and **creates** the necessary Datacom/DB definitions **automatically**. The resulting relational DBMS **design** reproduces the functions supported by the old DL/1 data base. The Subsystem then populates...

10/3,K/25      (Item 1 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
(c) 2005 ProQuest Info&Learning. All rts. reserv.

01147304 97-96698

**Drawings in a snap**

Schmitz, Barbara M

Computer-aided Engineering v14n12 PP: 20 Dec 1995

ISSN: 0733-3536 JRNL CODE: CAE

WORD COUNT: 188

ABSTRACT: Synthesis MKBS automated drafting and **design software** allows users to **create** custom, **configured** CAD manufacturing **drawings automatically**. The **software** - compatible with AutoCAD Release 12 and 13, for both DOS and Windows - does not require...

TEXT: Synthesis MKBS automated drafting and **design software** allows users to **create** custom, **configured** CAD manufacturing **drawings automatically**. The **software** --compatible with AutoCAD Release 12 and 13, for both DOS and Windows--does not require...

10/3,K/26 (Item 2 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

(c) 2005 ProQuest Info&Learning. All rts. reserv.

00908025 95-57417

**CAD/CAM, control systems, and software**

Owen, Jean V

Manufacturing Engineering v113n2 PP: 197-215 Aug 1994

ISSN: 0361-0853 JRNL CODE: MFE

WORD COUNT: 5028

...TEXT: database of associated design and manufacturing data, user variables, and process control intelligence. When a **design** or manufacturing **specification** changes, the **software automatically generates** new part programs for component parts and updates material requirement lists and manufacturing schedules. The...

10/3,K/27 (Item 3 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

(c) 2005 ProQuest Info&Learning. All rts. reserv.

00800862 94-50254

**Bank of Montreal: The quest for CASE**

Blackwell, Gerry

I.T. Magazine v25n11 PP: 22-26+ Nov 1993

ISSN: 0008-3364 JRNL CODE: CAD

WORD COUNT: 2765

...TEXT: the application must do. Most vendors were more interested in selling lower CASE tools for **automatically generating program code** from **design specifications**.

"We weren't looking for code generators," Mr. Hanspal says of that period in the...

10/3,K/28 (Item 1 from file: 20)

DIALOG(R)File 20:Dialog Global Reporter

(c) 2005 The Dialog Corp. All rts. reserv.

10463123 (USE FORMAT 7 OR 9 FOR FULLTEXT)

Sylvia Keys

12-May-05 01:00 PM

**Click for customer**

ENGINEER, p28

March 31, 2000

JOURNAL CODE: FTEN LANGUAGE: English RECORD TYPE: FULLTEXT

WORD COUNT: 458

(USE FORMAT 7 OR 9 FOR FULLTEXT)

... standard options.

If the standard options don't meet the customer's requirement, they can **design** a new **configuration**, and the in-KEY **software** will **generate** an almost **instant** bid for the job.

The aim is to take some of the risk, such as...

**10/3,K/29 (Item 2 from file: 20)**

DIALOG(R)File 20:Dialog Global Reporter

(c) 2005 The Dialog Corp. All rts. reserv.

09613081 (USE FORMAT 7 OR 9 FOR FULLTEXT)

**Designers do the knowledge**

ENGINEER

February 04, 2000

JOURNAL CODE: FTEN LANGUAGE: English RECORD TYPE: FULLTEXT

WORD COUNT: 1647

(USE FORMAT 7 OR 9 FOR FULLTEXT)

... satisfy this requirement.

KNOWLEDGEWARE

With Catia's Knowledgware, designers can create scripts to define a **design** process and **specifications**. The **software** will then **create** components meeting the specifications **automatically**.

WAVE

In Unigraphics' Wave, **design** parameters - such as the distance between bumper and radiator - can be specified in a 'control...

**10/3,K/30 (Item 3 from file: 20)**

DIALOG(R)File 20:Dialog Global Reporter

(c) 2005 The Dialog Corp. All rts. reserv.

08482428 (USE FORMAT 7 OR 9 FOR FULLTEXT)

**Bricsnet Launches the Most Comprehensive E-Marketplace for the Building Industry**

BUSINESS WIRE

December 01, 1999

JOURNAL CODE: WBWE LANGUAGE: English RECORD TYPE: FULLTEXT

WORD COUNT: 1284

(USE FORMAT 7 OR 9 FOR FULLTEXT)

... industry standard DWG file format. Based on the ACIS solid modeling kernel, Bricsnet's architectural **design software** can **automatically create** accurate quantities, **specifications** and engineering **drawings**. Users and developers can create intelligent building components with Visual Basic for Applications that can...

**10/3,K/31 (Item 1 from file: 95)**



DIALOG(R)File 95:TEME-Technology & Management  
(c) 2005 FIZ TECHNIK. All rts. reserv.

01325140 I99071669300

**T-VEC(exp TM) product summary**

anonym

Proceedings. 2nd IEEE Workshop on Industrial Strength Formal Specification Techniques, 21-23 Oct. 1998, Boca Raton, FL, USA1999

Document type: Conference paper Language: English

Record type: Abstract

ISBN: 0-7695-0081-1

**ABSTRACT:**

...tasks. Our approach is based on automated specification-based testing. The T-VEC toolset performs **automatic** test vector and test driver **generation** from **software** requirement and **design specifications**. In addition the toolset provides powerful specification analysis capabilities to ensure consistency and completeness early...

**10/3,K/32 (Item 2 from file: 95)**

DIALOG(R)File 95:TEME-Technology & Management

(c) 2005 FIZ TECHNIK. All rts. reserv.

01144571 E97091562080

**Application of a generator-based software development method supporting model reuse**

(Anwendung eines generator-basierten Software-Entwicklungsmodells, das die Modellwiederverwendung unterstuetzt)

Altmeyer, J; Riegel, JP; Schuermann, B; Schuetze, M; Zimmermann, G

Univ. of Kaiserslautern, D

CAiSE 97, Adv. Information Syst. Engineering, 9th Internat. Conf., Proc., Barcelona, E, Jun 16-20, 19971997

Document type: Conference paper Language: English

Record type: Abstract

ISBN: 3-540-63107-0

DESCRIPTORS: OBJECT ORIENTED PROGRAMMING; PROGRAM DEVELOPMENT; SYSTEMS **DESIGN** ; SOFTWARE TOOLS; **AUTOMATIC** PROGRAMMING; **PROGRAM GENERATORS** ; THEORETICAL MODELS; FORMAL **SPECIFICATION** ; **PROGRAM** REUSABILITY

**10/3,K/33 (Item 3 from file: 95)**

DIALOG(R)File 95:TEME-Technology & Management

(c) 2005 FIZ TECHNIK. All rts. reserv.

01024462 E96097236080

**The Care toolset for developing verified programs from formal specifications**

(Die Care Werkzeugkiste zur Entwicklung verifizierter Programme aus formalen Spezifikationen)

Hemer, D; Lindsay, P

Univ. of Queensland, St. Lucia, AUS

Proc. of the 4th Internat. Symp. on Assessment of Software Tools, Toronto, CDN, May 22-24, 19961996

Document type: Conference paper Language: English

Record type: Abstract

ISBN: 0-8186-7389-3

DESCRIPTORS: SOFTWARE RELIABILITY; SOFTWARE QUALITY; **AUTOMATIC**

PROGRAMMING; PROGRAM **GENERATORS** ; COMPUTER AIDED PROGRAMMING; PROGRAMMING SYSTEM; **SOFTWARE** TOOLS; PROGRAMMING ENVIRONMENTS; FORMAL **SPECIFICATION** ; DESCRIPTION LANGUAGES; PROGRAMMING AID; **DESIGN** LANGUAGES; MATHEMATICAL PROOF; SYSTEMS DESIGN; DOCUMENTATION; CONVERSATIONAL SYSTEMS; PROGRAM DEVELOPMENT; SOURCE PROGRAM

10/3,K/34 (Item 4 from file: 95)

DIALOG(R)File 95:TEME-Technology & Management  
(c) 2005 FIZ TECHNIK. All rts. reserv.

00698371 E93083154046

**Knowledge-based constraint-driven software synthesis**

(Wissensbasierte bedingungsgetriebene Software-Synthese)

Smith, TE; Setliff, DE

Univ. of Pittsburgh, USA

KBSE '92, The 7th Knowledge-Based Software Engineering Conf., IEEE, McLean, VA, USA, Sept. 20-23, 1992

Document type: Conference paper Language: English

Record type: Abstract

ISBN: 0-8186-2880-4

DESCRIPTORS: PROGRAM DEVELOPMENT; EXPERT SYSTEMS; REAL TIME METHOD; SYSTEMS **DESIGN** ; SYSTEM ARCHITECTURE; KNOWLEDGE REPRESENTATION; IMPLEMENTATION; **PROGRAM GENERATORS** ; FORMAL **SPECIFICATION** ; **AUTOMATIC** PROGRAMMING; **PROGRAM** REUSABILITY

10/3,K/35 (Item 5 from file: 95)

DIALOG(R)File 95:TEME-Technology & Management  
(c) 2005 FIZ TECHNIK. All rts. reserv.

00582739 I92025138938

**Experiments with an efficient heuristic algorithm for local microcode generation**

(Untersuchungen eines effizienten heuristischen Algorithmus zur Erzeugung lokaler Mikrocode-Programme)

Mahmood, M; Mavaddat, F; Elmastry, MI

Waterloo Univ., Ont., Canada

Proceedings. 1990 IEEE International Conference on Computer Design: VLSI in Computers and Processors, 17-19 Sept. 1990, Cambridge, MA, USA1990

Document type: Conference paper Language: English

Record type: Abstract

ISBN: 0-8186-2079-X

...DESCRIPTORS: MEMORY; MICROPROCESSORS; CIRCUIT CAD; COMPUTER AIDED **DESIGN** ; PROGRAM DEVELOPMENT; MICROELECTRONICS; **PROGRAM GENERATORS** ; **AUTOMATIC** PROGRAMMING; FORMAL **SPECIFICATION**

10/3,K/36 (Item 6 from file: 95)

DIALOG(R)File 95:TEME-Technology & Management  
(c) 2005 FIZ TECHNIK. All rts. reserv.

00527535 I91105897927

**KMDS: an expert system for integrated hardware/software design of microprocessor-based digital systems**

(KMDS: Ein Expertensystem fuer die integrierte

Hardware/Software-Entwicklung von auf Mikroprozessoreinsatz gegruendeten digitalen Systemen)

Yau-Hwang Kuo; Ling-Yang Kung; Ching-Chung Tzeng; Guang-Huei Jeng; Wei-Kuo Chia

Nat. Cheng Kung Univ., Tainan, Taiwan

IEEE Micro, v11, n4, pp32-35, 86-92, 1991

Document type: journal article Language: English

Record type: Abstract

ISSN: 0272-1732

ABSTRACT:

...from the existence of a large number of candidate solutions under a very high-level **design specification**. KMDS **automatically generates** a control **program**, making the fully automatic **design** of digital systems possible.

10/3,K/37 (Item 1 from file: 810)

DIALOG(R)File 810:Business Wire

(c) 1999 Business Wire . All rts. reserv.

0410179 BW767

**ANDERSEN CONSULTING: Foundation Announces First Tool To Support Hundreds Of Concurrent Client/Server Systems Developers**

June 6, 1994

...FCP Version 2.0-LP Windows Client Option enables users of the new product to **automatically generate** enterprise-wide client/server **applications** -- from the same **design specifications** -- for both the Windows Version 3.1 and OS/2 Presentation Manager (PM) environments.  
At...

10/3,K/38 (Item 1 from file: 635)

DIALOG(R)File 635:Business Dateline(R)

(c) 2005 ProQuest Info&Learning. All rts. reserv.

0167371 90-50601

**Top Defense Contractors in Austin: Ranked by Value of Contracts**

McDonald, Kathryn

Austin Business Journal (Austin, TX, US), V10 N31 s1 p12

PUBL DATE: 900924

WORD COUNT: 1,167

DATELINE: Austin, TX, US

TEXT:

...755

GOODS/SERVICES PROVIDED: Advanced software engineering with emphasis on graphical, object-oriented rapid prototyping; **automatic generation** of code from **specifications**; and methodologies for **software re-use** of **specifications, designs** and code

NUMBER OF EMPLOYEES: 34

HEAD OF AUSTIN OPERATION: Priscilla Yeh, president

FIRM NAME...

10/3,K/39 (Item 2 from file: 635)  
DIALOG(R)File 635:Business Dateline(R)  
(c) 2005 ProQuest Info&Learning. All rts. reserv.

0096763 89-20580

**Oracle's New CASE Generator Expands CASE Toolset; Major Productivity Gains  
Expected**

Sheeran, Lisa; Mollen, Alex  
Business Wire (San Francisco, CA, US) sl pl  
PUBL DATE: 890530  
WORD COUNT: 572  
DATELINE: Belmont, CA, US

TEXT:

...These two products, along with Oracle's CASE Method, were introduced in September 1988.

CASE Generator provides the ability to automatically generate advanced, portable applications directly from design specifications . In this first release, CASE Generator receives definitions from CASE Dictionary and translates the information...  
?

EXAMINER'S  
SEARCH NOTES

*See A'ed  
articles herein  
cg7 11/10/05*

File 344:Chinese Patents Abs Aug 1985-2004/May  
(c) 2004 European Patent Office  
File 347:JAPIO Nov 1976-2005/Jan(Updated 050506)  
(c) 2005 JPO & JAPIO  
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200529  
(c) 2005 Thomson Derwent  
File 348:EUROPEAN PATENTS 1978-2005/May W01  
(c) 2005 European Patent Office  
File 349:PCT FULLTEXT 1979-2005/UB=20050505,UT=20050428  
(c) 2005 WIPO/Univentio  
File 331:Derwent WPI First View UD=200529  
(c) 2005 Thomson Derwent  
File 371:French Patents 1961-2002/BOPI 200209  
(c) 2002 INPI. All rts. reserv.

Set	Items	Description
S1	10704	SWITCHGEAR?
S2	42920	(SOFTWARE OR APPS OR APPLICATIONS OR PROGRAM OR PROGRAMS) (-5N) (CONFIGUR? OR SPECIFICATION? OR SPEC OR SPECS)
S3	121976	(AUTOMATIC? OR SIMULTAN? OR INSTANT? OR IMMEDIAT? OR INSTANTAN? OR ON(1W)FLY) (5N) (CREAT? OR GENERAT? OR PRODUCING OR PRODUCE? OR DEVELOP? ?)
S4	5587341	DRAWING? OR SCHEMATIC? OR DESIGN?
S5	71	AU=(LESLIE, D? OR LESLIE D? OR LAUFENBERG, R? OR LAUFENBERG R?)
S6	42	S1 AND S2
S7	11	S6 AND S3
S8	11	S7 AND S4
S9	203	S2(5N)S3
S10	16	S9(5N)S4
S11	16	S10 NOT S8
S12	0	S5 AND S1
S13	4	S5 AND S2

*Considered cg7 11/10/05*

8/3,K/1 (Item 1 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2005 European Patent Office. All rts. reserv.

00602175

Overcurrent protection device with visual indicators for trip and programming functions

Überstromschutz mit visuellen Indikatoren für Auslösung und Programmierfunktionen

Dispositif de protection contre surintensite avec indicateurs visuels de declenchement et fonctions de programmation

PATENT ASSIGNEE:

EATON CORPORATION, (218422), Eaton Center, 1111 Superior Avenue,  
Cleveland Ohio 44114, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Lagree, James Leo, 709 Bar Harbor Drive, Pittsburgh, PA 15239, (US)

Matsko, Joseph Jacob, 645 Seventh Street, Beaver, PA 15009, (US)

LEGAL REPRESENTATIVE:

van Berlyn, Ronald Gilbert (37011), 23, Centre Heights, London NW3 6JG,  
(GB)

PATENT (CC, No, Kind, Date): EP 596643 A1 940511 (Basic)  
EP 596643 B1 970528

APPLICATION (CC, No, Date): EP 93308541 931027;

PRIORITY (CC, No, Date): US 969731 921027

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: H02H-003/093; H02H-003/04;

ABSTRACT WORD COUNT: 84

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB97	569
CLAIMS B	(German)	EPAB97	548
CLAIMS B	(French)	EPAB97	613
SPEC B	(English)	EPAB97	76477
Total word count - document A			0
Total word count - document B			78207
Total word count - documents A + B			78207

LEGAL STATUS (Type, Pub Date, Kind, Text):

...applicant **designated** states: DE;FR;GB;IT)  
Examination...

...applicant **designated** states: DE;FR;GB;IT)

...SPECIFICATION an electrical circuit interrupting device including an overcurrent trip unit, preferably for a metal clad **switchgear**, molded case circuit breaker and the like, protecting electrical conductors from damage due to excessive...

...the electrical distribution system. More specifically, the various molded case circuit breakers and metal clad **switchgear** breakers within an electrical distribution system are generally adapted to operate a predetermined number of...The invention will now be described, by way of example, with reference to the attached **drawings**, wherein:

FIG. 1 is a single line diagram of a 4.16 kV radial distribution...

...of the output current waveform of a saturated current transformer;

FIGS. 10-16 represent a **schematic** representation of the overcurrent tripping device;

FIGS. 17-31 represent a flow chart for the...

...the comparator subsystem and A/D input subsystems of the IC;

FIG. 57 is a **schematic** diagram of the quadcomparator subsystem;

FIG. 58 is a **schematic** diagram of the address decode logic for the comparator control registers CMPST and CMPI and...

...CFR;

FIG. 59 is a block diagram of the analog subsystem;

FIG. 60 is a **schematic** diagram of the microprocessor bus interface logic;

FIG. 61 is a **schematic** diagram of the address decode logic for the microprocessor bus interface registers;

FIG. 62 is a **schematic** diagram of control and status registers;

FIG. 63 is a block diagram of the analog...

...FIG. 64 is a block diagram of the analog control logic;

FIG. 65 is a **schematic** diagram of the current multiplexer (MUX) control logic;

FIG. 66 is a **schematic** diagram of the voltage MUX control logic;

FIG. 67 is a **schematic** diagram of the auto-zero registers;

FIG. 68 is a **schematic** diagram of a five microsecond timer;

FIG. 69 is a **schematic** diagram of the auto-zero control logic;

FIG. 70 is a **schematic** diagram of the auto-range control logic;

FIG. 71 is a **schematic** diagram of the ...FIG. 72 is an overall block diagram of the analog circuitry;

FIG. 73 is a **schematic** diagram of the input MUX system;

FIG. 74 is a block diagram of the quad comparator system;

FIG. 75 is a **schematic** diagram of a band gap regulator;

FIG. 76A is a **schematic** diagram of a shunt regulator, B+ comparator and a power monitor;

FIG. 76B is a **schematic** diagram of exemplary external conditioning circuitry and power supply circuitry for use with the IC;

FIG. 76C is a **schematic** diagram of an exemplary external regulator circuit for use with the IC;

FIG. 77 is a **schematic** diagram of a biasing circuit;

FIG. 78 is a **schematic** diagram of another biasing circuit;

FIG. 79 is a **schematic** diagram of an analog temperature sensing circuit;

FIG. 80 is a **schematic** diagram of the ranging circuitry for the voltage amplifier;

FIG. 81 is a **schematic** diagram of the current mirror and amplifier;

FIG. 82 is a **schematic** diagram of the current mirror;

FIG. 83 is a **schematic** diagram of the offset correction circuitry;

FIG. 84 is a **schematic** diagram of the auto-zeroable voltage and current amplifiers;

FIG. 85 is a block diagram...

...a digital demodulator which forms a portion of the communication controller;

FIG. 87 is a **schematic** diagram of a master clock generator which forms a portion of the communication controller;

FIG. 88 is a **schematic** diagram of a bit phase timing generator which forms a portion of the communication controller;

FIG. 89 is a **schematic** diagram of a receiver correlator which forms a portion of the communication controller;

FIG. 90 is a partial **schematic** diagram of a correlator counter which forms a portion of the communication controller;

FIG. 91 is the remaining portion of the **schematic** diagram of a correlator counter which forms a portion of the communication controller;

provide efficient connection of peripheral devices that communicate over a serial bus. It may...device, the MSTR control bit in the SPCR is set to a one by the **program** to **configure** the device to receive data on its MISO pin. In the slave device, its MISO...master device can detect that a fatal collision occurred if the software protocol is appropriately **designed**.

Since the slave mode device is operating asynchronously with the master device, the WCOL bit...

...be used as an indicator of a collision occurrence. The software communication protocol should be **designed** to accommodate the collisions that may be generated by this asynchronous operation.

The WCOL bit...read-write register is used to control operation of the ICC 29. It has been **designed** as a read-write register to permit read-modify-write instructions to operate on it...and address fields that are to be interpreted by the ICC.

\* A zero control bit **designates** that bits B3 through B26 contain arbitrary data, and are part of a higher-level...power to the analog portion of the IC 10. The analog supply pin AVDD is **designed** to be connected to a current source. The IC 10 contains an internal shunt regulator...

8/3,K/2 (Item 2 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00598933

**Overcurrent protection device**

**Überstromschutzeinrichtung**

**Dispositif de protection contre surintensite**

PATENT ASSIGNEE:

EATON CORPORATION, (218424), Eaton Center, 1111 Superior Avenue,  
Cleveland, Ohio 44114-2584, (US), (Proprietor designated states: all)

INVENTOR:

Lagree, James Leo, 709 Bar Harbor Drive, Pittsburgh, PA 15239, (US)  
Wargo, Harry William, 1547 Holly Hill Drive, Bethel Park, PA 15102, (US)  
Tomeo, Anthony, 362 Stoneledge Drive, Pittsburgh, PA 15235, (US)  
Engel, Joseph Charles, 107 Overlook Circle, Monroeville, PA 15146, (US)

LEGAL REPRESENTATIVE:

van Berlyn, Ronald Gilbert (37011), 23, Centre Heights, London NW3 6JG,  
(GB)

PATENT (CC, No, Kind, Date): EP 577339 A1 940105 (Basic)

EP 577339 B1 991006

APPLICATION (CC, No, Date): EP 93304947 930624;

PRIORITY (CC, No, Date): US 907131 920630

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: H02H-003/093

ABSTRACT WORD COUNT: 141

NOTE:

Figure number on first page: 4

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9940	139
CLAIMS B	(German)	9940	108
CLAIMS B	(French)	9940	144
SPEC B	(English)	9940	75775
Total word count - document A			0



Total word count - document B 76166  
Total word count - documents A + B 76166

LEGAL STATUS (Type, Pub Date, Kind, Text):  
...applicant **designated** states: DE;FR;GB;IT)  
Examination...

...applicant **designated** states: DE;FR;GB;IT)  
Grant...

...SPECIFICATION to an overcurrent trip unit for an electrical circuit interrupting device, such as metal clad **switchgear**, molded case circuit breakers and the like, for protecting electrical conductors from damage due to...

...the electrical distribution system. More specifically, the various molded case circuit breakers and metal clad **switchgear** breakers within an electrical distribution system are generally adapted to operate a predetermined number of...

...fuse with other overcurrent devices over an entire range of anticipated overcurrents. Thus, in such **applications**, the coordinated protection may be less than ideal.

Another problem with known overcurrent devices relates...selectable function including a long time delay portion, a short time delay portion and an **instantaneous** portion, characterised by means for adjusting said long time delay portion and said short time...

...unit for an electrical interrupting device, such as a molded case circuit breaker, metal clad **switchgear** breaker or the like and more particularly to a microprocessor based overcurrent trip unit which...

...now be described, by way of example, with reference to the following description and attached **drawings**, wherein:

FIG. 1 is a single line diagram of a 4.16 kV radial distribution...the comparator subsystem and A/D input subsystems of the IC;

FIG. 57 is a **schematic** diagram of the quadcomparator subsystem;

FIG. 58 is a **schematic** diagram of the address decode logic for the comparator control registers CMPST and CMPI and...

...CFR;

FIG. 59 is a block diagram of the analog subsystem;

FIG. 60 is a **schematic** diagram of the microprocessor bus interface logic;

FIG. 61 is a **schematic** diagram of the address decode logic for the microprocessor bus interface registers;

FIG. 62 is a **schematic** diagram of control and status registers;

FIG. 63 is a block diagram of the analog...

...FIG. 64 is a block diagram of the analog control logic;

FIG. 65 is a **schematic** diagram of the current multiplexer (MUX) control logic;

FIG. 66 is a **schematic** diagram of the voltage MUX control logic;

FIG. 67 is a **schematic** diagram of the auto-zero registers;

FIG. 68 is a **schematic** diagram of a five microsecond timer;

FIG. 69 is a **schematic** diagram of the auto-zero control logic;

FIG. 70 is a **schematic** diagram of the auto-range control logic;

FIG. 71 is a **schematic** diagram of the auto-range state machine;

FIG. 72 is an overall block diagram of the analog circuitry;

FIG. 73 is a **schematic** diagram of the input MUX system;

FIG. 74 is a block diagram of the quad comparator system;

Hexadecimal. Base 16 numbers written with a dollar sign prefix. For example, \$0100 = 256 decimal.

High-true: These signals are **designed** with the suffix "h" and are defined to be asserted (true or logical 1) when...

...level is at or near zero volts direct current (Vdc).

Low-true: These signals are **designed** by the suffix "b" and are defined to be asserted (true, or a logical 1...

...IC 10.

Output: An output signal is driven by the IC 10.

Referring to the **drawings**, the IC in accordance with the present invention is generally identified with the reference numeral...portion of an exemplary embodiment of the IC 10 is shown in FIG. 33. Various **configuration** options are selectable by **software** programming and mask options for specific applications as will be discussed below. In order to ...IC 10 for a specific application. The configuration information is defined by either mask options, **software**, constants or run time **configuration**.

Regarding mask options, the contents of the ROM 38 may be specified at the time...

...always an output and MISO is always an input.

In addition to the mask options, **software** constants are also used for **configuration** of the IC 10. More specifically, internal configuration registers are loaded by the microprocessor software...parallel or serial fashion.

The configuration registers CFR and ACFR are used to specify various **software configuration** options available in the IC 10 architecture. These registers CFR, ACFR are programmed during **software** initialization to **configure** input/output pins to their appropriate function as well as setting other major configuration parameters...phase 2 clock. It can be used for many purposes, including input waveform measurements, while **simultaneously generating** an output waveform. Pulse widths can vary from several microseconds to many seconds. The timer...has been disabled in the IC 10.

The serial peripheral interface (SPI) subsystem 54 is **designed** to provide efficient connection of peripheral devices that communicate over a serial bus. It may...device, the MSTR control bit in the SPCR is set to a one by the **program** to **configure** the device to receive data on its MISO pin. In the slave device, its MISO...master device can detect that a fatal collision occurred if the software protocol is appropriately **designed**.

...Since the slave mode device is operating asynchronously with the master device, the WCOL bit...

...be used as an indicator of a collision occurrence. The software communication protocol should be **designed** to accommodate the collisions that may be generated by this asynchronous operation.

The WCOL bit...read-write register is used to control operation of the ICC 29. It has been **designed** as a read-write register to permit read-modify-write instructions to operate on it...and address fields that are to be interpreted by the ICC.

\* A zero control bit **designates** that bits B3 through B26 contain arbitrary data, and are part of a higher-level...power to the analog portion of the IC 10. The analog supply pin AVDD is **designed** to be connected to a current source. The IC 10 contains an internal shunt regulator...

DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2005 European Patent Office. All rts. reserv.

00209007

Apparatus for data transmission from multiple sources on a single channel.  
Gerat zur Übertragung von Daten mehrerer Signalquellen auf einem einzigen Übertragungskanal.

Appareil de transmission de donnees provenant de sources multiples sur un canal unique.

PATENT ASSIGNEE:

NIAGARA MOHAWK POWER CORPORATION, (602610), 300 Erie Boulevard West,  
Syracuse New York 13202, (US), (applicant designated states:  
AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

Smith-Vaniz, William Reid, 14 Pasture Lane Darien, Connecticut 06820,  
(US)

LEGAL REPRESENTATIVE:

Blatchford, William Michael et al (48801), Withers & Rogers 4 Dyer's  
Buildings Holborn, London EC1N 2JT, (GB)

PATENT (CC, No, Kind, Date): EP 218222 A2 870415 (Basic)  
EP 218222 A3 880302  
EP 218222 B1 910612

APPLICATION (CC, No, Date): EP 86113756 840412;

PRIORITY (CC, No, Date): US 484681 830413

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):  
EP 125796

INTERNATIONAL PATENT CLASS: G01R-015/07; H04B-003/54;

ABSTRACT WORD COUNT: 307

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	638
CLAIMS B	(German)	EPBBF1	533
CLAIMS B	(French)	EPBBF1	703
SPEC B	(English)	EPBBF1	13402
Total word count - document A			0
Total word count - document B			15276
Total word count - documents A + B			15276

...ABSTRACT the state of the power system. Appropriate control signals are transmitted back to the electrical **switchgear** of the system to bring it to the appropriate optimum state. Direct local control may...

...SPECIFICATION Reactive Power Dispatch

4. Security (i.e. Stability) Constrained Economic Dispatch

5. Contingency Analysis

6. **Automatic Generation** Control and Minimum Area Control Error

7. Dynamic System Security Analysis

8. Energy Interchange Billing...State Estimator which then issues appropriate control signals over other transmission links 33 to the **switchgear** 58 at electrical substations 44. Thus the power supply to transmission lines may be varied...

...therewith local control apparatus 36 for controlling the illustrative transformer bank 38 and the electrical **switchgear** indicated by the small squares 40. The modules 20 may be mounted to live conductors... form.

The invention will now be described by way of example with reference to

the **drawings** in which:-

Figure 1 is a perspective view of a state estimator module installed on...

...conductor;

Figure 4 is a diagrammatic view of a substation;

Figure 5 is a diagrammatic **schematic** view of a monitored and controlled power delivery system in accordance with the invention;

Figure thereof;

Figure 28 is a **schematic** block diagram of the electronics of a state estimator ;

Figure 29 is a detailed **schematic** electrical circuit diagram of the power supply of the state estimator;

Figure 30 is a detailed electrical **schematic** block diagram of a portion of the electronics illustrated in Figure 28;

Figure 31, comprising...

...through 31D which may be put together as shown in Figure 31E, is a detailed **schematic** electrical circuit diagram of the electronics shown in Figure 30;

Figures 32 and 33 are **schematic** electrical circuit diagrams illustrating a voltage measurement system;

Figure 34 is a timing diagram of...

...The same reference characters refer to the same elements throughout the several views of the **drawings** .

The State Estimator Module

General

The state estimator modules 20 ("Donuts") clamp to a high...

...spoke area in the center of the donut 20 and the temperature probe placement are **designed** with as much free space as possible so as not to affect the temperature of...sensors 296 provide analog signals as indicated at 304 to the electronics 294. The detailed **schematic** electrical circuit diagram of the power supply 292 is shown in Figure 29.

Figure 30 is a **schematic** block diagram of the electronics 294. As shown therein, the Rogowski coil 80 is connected...

...to a 950 megahertz transmitter 96 which then supplies it to the antenna 98.

The **schematic** electrical circuit diagram of the electronics 294 is shown in Figure 31, comprising Figures 31A...20 (sometimes called herein the substation monitor) is a MC146805E2 microprocessor device.

Introduction

The "Donut" **software specification** is divided into three major sections, reflecting the three tasks performed by the software. They...

8/3,K/4 (Item 4 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00209006

**Apparatus for measuring the potential of a transmission conductor.**

**Gerat zur Spannungsmessung an einer Ubertragungsleitung.**

**Appareil de mesure du potentiel d'une ligne de transmission.**

PATENT ASSIGNEE:

NIAGARA MOHAWK POWER CORPORATION, (602610), 300 Erie Boulevard West,  
Syracuse New York 13202, (US), (applicant designated states:

Sylvia Keys

11-May-05 03:58 PM

AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)  
 INVENTOR:  
 Smith-Vaniz, William Reid, 14 Pasture Lane, Darien Connecticut 06820,  
 (US)  
 Sieron, Richard Leonard, 1470 Melville Avenue, Fairfield Connecticut  
 06430, (US)  
 LEGAL REPRESENTATIVE:  
 Blatchford, William Michael et al (48801), Withers & Rogers 4 Dyer's  
 Buildings Holborn, London EC1N 2JT, (GB)  
 PATENT (CC, No, Kind, Date): EP 218221 A2 870415 (Basic)  
 EP 218221 A3 880302  
 EP 218221 B1 910904  
 APPLICATION (CC, No, Date): EP 86113755 840412;  
 PRIORITY (CC, No, Date): US 484681 830413  
 DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE  
 RELATED PARENT NUMBER(S) - PN (AN):  
 EP 125796  
 INTERNATIONAL PATENT CLASS: G01R-015/06; G01R-015/02;  
 ABSTRACT WORD COUNT: 305

LANGUAGE (Publication,Procedural,Application): English; English; English  
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	611
CLAIMS B	(German)	EPBBF1	597
CLAIMS B	(French)	EPBBF1	637
SPEC B	(English)	EPBBF1	17787
Total word count - document A			0
Total word count - document B			19632
Total word count - documents A + B			19632

...ABSTRACT the state of the power system. Appropriate control signals are transmitted back to the electrical **switchgear** of the system to bring it to the appropriate optimum state. Direct local control may...

...SPECIFICATION Reactive Power Dispatch  
 4. Security (i.e. Stability) Constrained Economic Dispatch  
 5. Contingency Analysis  
 6. **Automatic Generation** Control and Minimum Area Control Error  
 7. Dynamic System Security Analysis  
 8. Energy Interchange Billing...State Estimator which then issues appropriate control signals over other transmission links 33 to the **switchgear** 58 at electrical substations 44. Thus the power supply to transmission lines may be varied...

...detected and the power supplied to the transformer controlled by the Dynamic State Estimator through **switchgear** .  
 A Dynamic State Estimator may be located at one or more substations to control the...

...therewith local control apparatus 36 for controlling the illustrative transformer bank 38 and the electrical **switchgear** indicated by the small squares 40. The modules 20 may be mounted to live conductors... form.

The invention will now be described by way of example with reference to the **drawings** in which:-

Figure 1 is a perspective view of a state estimator module installed on...

...conductor;

Figure 4 is a diagrammatic view of a substation;

Figure 5 is a diagrammatic **schematic** view of a monitored and controlled power delivery system in accordance with the invention; Figure...

...a back view thereof;

Figure 27 is a side view thereof;

Figure 28 is a **schematic** block diagram of the electronics of a state estimator ;

Figure 29 is a detailed **schematic** electrical circuit diagram of the power supply of the state estimator ;

Figure 30 is a detailed electrical **schematic** block diagram of a portion of the electronics illustrated in Figure 28;

Figure 31, comprising...

...through 31D which may be put together as shown in Figure 31E, is a detailed **schematic** electrical circuit diagram of the electronics shown in Figure 30;

Figures 32 and 33 are **schematic** electrical circuit diagrams illustrating a voltage measurement system;

Figure 34 is a timing diagram of...The same reference characters refer to the same elements throughout the several views of the **drawings**

The State Estimator Module  
General

The state estimator modules 20 ("Donuts") clamp to a high...

...spoke area in the center of the donut 20 and the temperature probe placement are **designed** with as much free space as possible so as not to affect the temperature of...sensors 296 provide analog signals as indicated at 304 to the electronics 294. The detailed **schematic** electrical circuit diagram of the power supply 292 is shown in Figure 29.

Figure 30 is a **schematic** block diagram of the electronics 294. As shown therein, the Rogowski coil 80 is connected...

...to a 950 megahertz transmitter 96 which then supplies it to the antenna 98.

The **schematic** electrical circuit diagram of the electronics 294 is shown in Figure 31, comprising Figures 31A...20 (sometimes called herein the substation monitor) is a MCL46805E2 microprocessor device.

Introduction

The "Donut" **software specification** is divided into three major sections, reflecting the three tasks performed by the software. They... factor entry) will be performed through this link. Communications protocols will be defined in the **design spec**.

Error Detection / Action:

When each table is up/down loaded, a 16 bit CRC...

8/3,K/5 (Item 5 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

00209005

**Apparatus** for measuring and calculating fourier components of a power line parameter.

Gerat zur Messung und Berechnung von Fourier-Komponenten elektrischer Grossen in einer Hochspannungsleitung.

Appareil pour mesurer et calculer les coefficients de Fourier d'un parametre d'une ligne a haute tension.

Sylvia Keys

11-May-05 03:58 PM

PATENT ASSIGNEE:

NIAGARA MOHAWK POWER CORPORATION, (602610), 300 Erie Boulevard West,  
Syracuse New York 13202, (US), (applicant designated states:  
AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)

INVENTOR:

Smith-Vaniz, William Reid, 14 Pasture Lane, Darien Connecticut 06820,  
(US)

LEGAL REPRESENTATIVE:

Blatchford, William Michael et al (48801), Withers & Rogers 4 Dyer's  
Buildings Holborn, London EC1N 2JT, (GB)

PATENT (CC, No, Kind, Date): EP 218220 A2 870415 (Basic)  
EP 218220 A3 880309  
EP 218220 B1 910619

APPLICATION (CC, No, Date): EP 86113754 840412;

PRIORITY (CC, No, Date): US 484681 830413

DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 125796

INTERNATIONAL PATENT CLASS: G01R-015/07; G01R-019/25; G01R-023/16;

ABSTRACT WORD COUNT: 308

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	834
CLAIMS B	(German)	EPBBF1	812
CLAIMS B	(French)	EPBBF1	1077
SPEC B	(English)	EPBBF1	17875
Total word count - document A			0
Total word count - document B			20598
Total word count - documents A + B			20598

...ABSTRACT the state of the power system. Appropriate control signals are transmitted back to the electrical **switchgear** of the system to bring it to the appropriate optimum state. Direct local control may...

...SPECIFICATION Reactive Power Dispatch

4. Security (i.e. Stability) Constrained Economic Dispatch
5. Contingency Analysis
6. **Automatic Generation** Control and Minimum Area Control Error
7. Dynamic System Security Analysis
8. Energy interchange Billing...State Estimator which then issues appropriate control signals over other transmission links 33 to the **switchgear** 58 at electrical substations. 44. Thus the power supply to transmission lines may be varied...

...detected and the power supplied to the transformer controlled by the Dynamic State Estimator through **switchgear** .

A Dynamic State Estimator may be located at one or more substations to control the...

...therewith local control apparatus 36 for controlling the illustrative transformer bank 38 and the electrical **switchgear** indicated by the small squares 40. The modules 20 may be mounted to live conductors... claims.

The invention will now be described by way of example with reference to the **drawings** in which:-

Figure 1 is a perspective view of a state estimator module installed on...

...conductor;

Figure 4 is a diagrammatic view of a substation;  
Figure 5 is a diagrammatic **schematic** view of a monitored and controlled power delivery system in accordance with the invention;  
Figure...

...a back view thereof;

Figure 27 is a side view thereof;

Figure 28 is a **schematic** block diagram of the electronics of a state estimator;

Figure 29 is a detailed **schematic** electrical circuit diagram of the power supply of the state estimator;

Figure 30 is a detailed electrical **schematic** block diagram of a portion of the electronics illustrated in Figure 28;

Figure 31, comprising...

...through 31D which may be put together as shown in Figure 31E, is a detailed **schematic** electrical circuit diagram of the electronics shown in Figure 30;

Figures 32 and 33 are **schematic** electrical circuit diagrams illustrating a voltage measurement system;

Figure 34 is a timing diagram of...The same reference characters refer to the same elements throughout the several views of the **drawings**

The State Estimator Module

General

The state estimator modules 20 ("Donuts") clamp to a high...

...spoke area in the center of the donut 20 and the temperature probe placement are **designed** with as much free space as possible so as not to affect the temperature of...sensors 296 provide analog signals as indicated at 304 to the electronics 294. The detailed **schematic** electrical circuit diagram of the power supply 292 is shown in Figure 29.

Figure 30 is a **schematic** block diagram of the electronics 294. As shown therein, the Rogowski coil 80 is connected...

...to a 950 megahertz transmitter 96 which then supplies it to the antenna 98.

The **schematic** electrical circuit diagram of the electronics 294 is shown in Figure 31, comprising Figures 31A...20 (sometimes called herein the substation monitor) is a MC146805E2 microprocessor device.

Introduction

The "Donut" **software specification** is divided into three major sections, reflecting the three tasks performed by the software. They... factor entry) will be performed through this link. Communications protocols will be defined in the **design spec**.

Error Detection / Action:

When each table is up/down loaded, a 16 bit CRC...

8/3,K/6 (Item 1 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

01222633

**METHODS, SYSTEMS AND APPARATUS FOR REGULATING FREQUENCY OF GENERATED POWER USING FLYWHEEL ENERGY STORAGE SYSTEMS WITH VARYING LOAD AND/OR POWER GENERATION**

**PROCEDES, SYSTEMES ET DISPOSITIF PERMETTANT DE REGULER LA FREQUENCE D'UNE**

Sylvia Keys

11-May-05 03:58 PM



PUISSANCE PRODUITE AU MOYEN DE SYSTEMES DE STOCKAGE D'ENERGIE PAR  
VOLANT D'INERTIE A CHARGE ET/OU A PUISSANCE VARIABLES

Patent Applicant/Assignee:

BEACON POWER CORPORATION, 234 Ballardvale Street, Wilmington, MA 01887,  
US, US (Residence), US (Nationality), (For all **designated** states  
except: US)

Patent Applicant/Inventor:

CAPP F William, 2900 Thamas Avenue, Apt. 2311, Minneapolis, MN 55416, US,  
US (Residence), US (Nationality), (**Designated** only for: US)

LAZAREWICZ Matthew L, 35 Lawrence Road, Boxford, MA, 01921, US, US  
(Residence), US (Nationality), (Designated only for: US)

ARSENEAUX James A, 12 Meadow Lane, Westford, MA 01886, US, US (Residence)  
, US (Nationality), (**Designated** only for: US)

DRESENS Paul, 155 Main Street, Wayland, MA 01778, US, US (Residence), US  
(Nationality), (**Designated** only for: US)

ROJAS Hernan Alex, 1 Salem Street, Salem, NH 03079, US, US (Residence),  
US (Nationality), (**Designated** only for: US)

Legal Representative:

DALEY William J (et al) (agent), Edwards & Angell, LLP, 101 Federal  
Street, P.O. Box 55874, Boston, MA 02205-5874, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200529667 A2 20050331 (WO 0529667)

Application: WO 2004US26721 20040816 (PCT/WO US04026721)

Priority Application: US 2003495499 20030815

Parent Application/Grant:

Related by Continuation to: US 2003495499 20030815 (CON)

Designated States:

(All protection types applied unless otherwise stated - for applications  
2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM  
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC  
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO  
RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US (patent) UZ VC VN YU  
ZA ZM ZW

(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PL PT RO  
SE SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 17024

Patent Applicant/Assignee:

... For all **designated** states except: US)

Patent Applicant/Inventor:

... **Designated** only for: US)

LAZAREWICZ Matthew L...

... **Designated** only for: US)

DRESENS Paul...

... **Designated** only for: US)

ROJAS Hernan Alex...

... **Designated** only for: US)

Fulltext Availability:

Detailed Description

Legal Status (Type, Date, Text)

...pages 1/23-23/23, **drawings** , replaced by new pages 1/23-23/23; due to late transmittal by the receiving...

#### Detailed Description

... power grids, regulation is a function or parameter that involves the use of on-line **generation** that is equipped with **automatic generation** control (AGC) and that can change output quickly (MW/minute) to track the moment-to...

...systems.

Other aspects and embodiments of the invention are discussed below.

#### BRIEF DESCRIPTION OF THE **DRAWING**

For a fuller understanding of the nature and desired objects of the present invention, reference is made to the following detailed description taken in conjunction with the accompanying **drawing** figures wherein like reference character denote corresponding parts throughout the several views and wherein.

Fig...

...according to the present invention for controlling the FES S array;

Fig. 8A is a **schematic** view of another embodiment of an FESS array according to the one aspect of the present invention;

Fig. 8B is a **schematic** view that more particularly illustrates the connection of the FES S array of Fig. 8A...

...a graphical view illustrating a regulation signal and set point; Fig. 11 is a **schematic** view of an FESS array according to other aspects of the present invention; Figs. 12A...

...present invention for controlling the FES S array of Fig. 10;

Fig. 13A is a **schematic** diagram of an array of a multiple of flywheel energy storage systems illustrating a master...

#### ...DESCRIPTION OF THE PREFERRED EMBODIMENT

15 Referring now to the various figures of the **drawing** wherein like reference characters refer to like parts, there is shown in Fig. 4 a...

...array is configured so as to be mobile (e.g., transportable) or a non-mobile **design** , and the room or space available at the site at which the FESS array is...the actual mechanical motor/generator system that is typically based on a permanent-magnet-brushless **design** and this electromechanical system is typically controlled and monitored by the flywheel controller 122. It...

...motor/ generator system to comprise any of a number of 15 other motor type **designs** including, but not limited to, reluctance motors and inductance motors. Such a flywheel energy storage...

...motor/ generator from the electrical power source. In this way, the flywheel controller 122 **automatically** maintains the power **producing** capacity of the FES S unit so as to be in a desired range when...

8/3,K/7 (Item 2 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2005 WIPO/Univentio. All rts. reserv.

01006574 \*\*Image available\*\*

**INTERFACE MODULE FOR A CONTROLLED COMPONENT**

**MODULE D'INTERFACE POUR UN COMPOSANT COMMANDE**

Patent Applicant/Assignee:

WESTINGHOUSE ELECTRIC COMPANY LLC, P.O. Box 355, Pittsburgh, PA  
15230-0355, US, US (Residence), US (Nationality)

Inventor(s):

BEDNAR Fred H, 261 Emerson Road, Pittsburgh, PA 15209, US,  
COOK Bruce M, 425 Tivoli Road, Pittsburgh, PA 15239, US,  
GAUSSA Louis W Jr, 3 Dover Road, Irwin, PA 15642, US,  
LANG Glenn E, 880 Winfield Road, Cabot, PA 16023, US,  
SCHAEFER William F, 911 First Street, North Huntingdon, PA 15642, US,  
SLINSKI Stephen, 1004 Franklin Street, McKeesport, PA 15132, US,

Legal Representative:

VALENTINE James C (et al) (agent), Westinghouse Electric Company LLC,  
P.O. Box 355, Pittsburgh, PA 15230-0355, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200336653 A1 20030501 (WO 0336653)

Application: WO 2002US25992 20020815 (PCT/WO US0225992)

Priority Application: US 2001324331 20010924; US 2001324332 20010924

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ  
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI  
SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW

(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 8557

Fulltext Availability:

Detailed Description

Detailed Description

... independently utilize diverse sensors and logic to generate control  
signals for pumps, motors, contactors, solenoids, **switchgear**, and the  
like. In addition, manual control signals are integrated with the  
automatic signals. It...

...control signal for the component. Typically, the manual signals are  
given the highest priority. Signals **generated** by the **automatic**  
systems can be assigned priority in two ...relay contacts are either  
both zero or both non-zero.

**5 BRIEF DESCRIPTION OF THE DRAWINGS**

A full understanding of ...from the following  
description of the preferred embodiments when read in conjunction with  
the accompanying **drawings** in which.

Figure 1 is a simplified block diagram of a plant incorporating a  
component...can be used for some other fatiction, manual or automatic,  
that is suited to its **design**. For instance, it could ...providing the

opposite actuation. Similarly, these two relays could be used to open and close **switchgear**. For some components only one relay is needed. For instance, in the case of a...unique functional requirements for that particular component. The CIM 3, on the other hand, is **designed** as a universal control interface. As will be seen, configuration jumpers are used to select...follows.

CINI Position of the component in the actuated state (valve open, pump running or **switchgear** closed)  
CIN2 Position of the component in the unactuated state (valve closed, pump stopped or **switchgear** open)  
CIN3 Low TRUE; Torque limit exceeded in the opening direction (may be combined with...the FPGA which uses these signals to select the dynamic logic signals. For nonsafety system **applications** of the CIM 3, the **configuration** settings may be established under control of the software of the "host" system (Port Y...CIN1 and CIN2 inputs have no affect on the logic but are passed to the **software** systems. If the JP6 **configuration** jumper is not in place, the CIN3 and CIN4 inputs are treated as simple torque...L e., the valve will continue to close until the torque switch opens if so **configured** .

There will be some **applications** of the CIM where the entire motor stop logic is done external to the CIM...is not inserted, the deenergization of CIM outputs at travel limit is immediate.

For electrical **switchgear** and large motors, it is necessary to prevent the breaker from "pumping" closed then open...

8/3,K/8 (Item 3 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00817015 \*\*Image available\*\*

**METHOD AND CIRCUIT FOR USING POLARIZED DEVICE IN AC APPLICATIONS**  
**PROCEDE ET CIRCUIT SERVANT A UN DISPOSITIF POLARISE, DANS DES APPLICATIONS EN COURANT ALTERNATIF**

Patent Applicant/Inventor:

DUFF William B Jr, 22 Shiloh Road, Odessa, TX 79762-8400, US, US  
(Residence), US (Nationality)

Legal Representative:

NORDSTROM Erik R (agent), Fulbright & Jaworski, L.L.P., 600 Congress Avenue, Suite 2400, Austin, TX 78701, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200150568 A1 20010712 (WO 0150568)

Application: WO 2000US33524 20001206 (PCT/WO US0033524)

Priority Application: US 2000174433 20000104; US 2000710998 20001109

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT  
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT TZ UA UG UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English  
Filing Language: English  
Fulltext Word Count: 21618

Fulltext Availability:  
Detailed Description

#### Detailed Description

##### DESCRIPTION

##### METHOD AND CIRCUIT FOR USING POLARIZED DEVICE IN AC

##### APPLICATIONS

This **specification** claims the benefit of provisional application Serial No..

60/174,433, entitled "Method and Circuit...

...the series impedance of the capacitor. This reduces fault currents and thus reduces generator, transformer, **switchgear**, bus and transmission line size requirements. The capacitor in series with the fault acts as... capacitor will cause a reverse current flow through zener diode 104. This electrical behavior is **schematically** modeled by depicting a zener diode 104 in parallel, but with opposite polarity alignment than...for using polarized capacitors in transient AC applications. As taught by Norbert, circuit 500 is **designed** to be primarily a phase shifter for starting single-phase asynchronous motors. The circuit 500...conception and specific embodiment disclosed may be readily utilized as a basis for modifying or **designing** other structures for carrying out the same purposes of the present invention. It should also...

...scope of the invention as set forth in the appended claims.

##### BRIEF DESCRIPTION OF THE **DRAWINGS**

For a more complete understanding of the present invention and the advantages thereof, reference is now made to the following descriptions taken in conjunction with the accompanying **drawings**, in which.

Figure I is a prior art circuit model for electrolytic capacitors.

Figure 2...another embodiment of the present invention, suitable for continuous operation.

Figure 23A is a simplified **schematic** of the charging mechanism of Figure 23.

Figure 24 depicts another embodiment of the present...operation. The details of the DC bias superposition circuitry are omitted for simplicity in this **drawing** but will be addressed in greater detail below. There are many circuit realizations suitable for...

...component heat build up, DC reference voltage oscillation and premature failures characteristic of prior art **applications**.

In an ideal **configuration**, the DC bias sources are electrically isolated from

independent of) the AC source. Accordingly, no...a low resistance placed in shunt. The DC bias source details are omitted from the **drawing** for simplicity. This **drawing** serves to illustrate that AC circuit elements may separate forward biased anti-series PECS devices...and AC load 940. The AC neutral and/or ground path is omitted from this **drawing** for simplicity. From an AC perspective, resistors 934, 935 are connected ...

outlined. The most economical realization will vary, according to... device anodes (or cathodes) connected substantially together at a DC junction node in current divider **designs**. For example, five PECS devices, in a star configuration with their anodes connected together, would...

...to eliminate plural AC sources.

The term "AC blocking device" shall include any device, method, **design** or technique that provides a relatively large AC impedance, as compared with associated anti-series...

...and the like.

The terms "DC", "DC electricity" and "DC current" may be any technology, **design**, condition, physical condition or device, creating, causing, contributing, supporting, or favoring a unidirectional or predominantly ...diode dropper devices' and precisely regulated floating DC power supply voltages can provide operational and **design** benefits, especially where electrochemical batteries are included for power source redundancy, or are the anti...

...source" is used in the broad sense. This term generally covers and includes any method, **design** and/or device used or useful in the production and distribution of DC voltage and...at the time of connection or operable connection, and is not necessarily intrinsic to the **design**, construction, materials or character of the power supplies.

The term "polarized electric charge storage" ("PECS...electrical current may be turned on or turned off. Switch shall include mechanical conductor contact **designs**, electromechanical devices, semiconductor devices, relays, liquid contact devices such as mercury switches, molecular switches, ionization...

...are illustrative and not limiting.

The term "DC blocking device" shall include any device, method, **design**, apparatus and/or technique that provides a relatively large DC resistance and/or opposition to...

8/3,K/9 (Item 4 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00734951 \*\*Image available\*\*

**ELECTRICAL DISTRIBUTION SYSTEM AUTOMATION  
AUTOMATISATION D'UN SYSTEME DE DISTRIBUTION ELECTRIQUE**

Patent Applicant/Assignee:

NU-LEC PTY LTD, 35-37 South Street, Lytton, QLD 4178, AU, AU (Residence),  
AU (Nationality), (For all **designated** states except: US)

Patent Applicant/Inventor:

MURRAY David Russell, 55 Waterloo Esplanade, Wynnum, QLD 4178, AU, AU  
(Residence), AU (Nationality), (**Designated** only for: US)

Legal Representative:

FISHER ADAMS KELLY, Level 13, AMP Place, 10 Eagle Street, Brisbane, QLD  
4000, AU

Patent and Priority Information (Country, Number, Date):

Patent: WO 200048288 A1 20000817 (WO 0048288)

Application: WO 2000AU74 20000208 (PCT/WO AU0000074)

Priority Application: AU 998555 19990208

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB  
GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA  
UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 12494

Patent Applicant/Assignee:

... For all **designated** states except: US)

Patent Applicant/Inventor:

... **Designated** only for: US)

Fulltext Availability:

Detailed Description

Claims

Detailed Description

... distribution network

configuration.

In a further aspect, the invention resides in a method for  
controlling **switchgear** in at least a portion of an electricity  
distribution

network, which network portion includes a...

...determined time delay.

In a further aspect the invention resides in a method for controlling  
**switchgear** in at least a portion of an electricity distribution  
network, which  
network portion includes a...In preference, a set of suggested steps for  
addressing a  
particular abnormal operating pattern is **automatically generated** to  
assist definition of a switching sequence.

The representation created by the user may be analysed in  
order to **automatically produce** further abnormal operating patterns to  
a  
user.

In a yet further aspect of the invention...

...devices for limiting the adverse effects of each abnormal operating  
pattern.

BRIEF DESCRIPTION OF THE **DRAWINGS**

To assist in understanding the invention preferred  
15 embodiments will now be described with...

...network portion of FIGS 1

and 2,

FIG. 6 is a block diagram of a **software** tool for **configuring**  
switching sequences for the control apparatus of the first embodiment;  
and

FIG. 7 is a...transformer and a capacitive voltage

Sylvia Keys

11-May-05 03:58 PM

transformer, which may be either external or internal to the **switchgear**. It will be appreciated that the communications link may alternatively be implemented using optical fibre...now be described in more detail to assist understanding of the invention. In the initial **design** phase, a user identifies a group of network devices in a particular portion of the...to a second aspect of the present invention. The second aspect is a automation scheme **configuration** tool, that is preferably a **software** application which  
Substitute Sheet (Rule 26) RO/AU  
can run on a personal computer to...

...between network components are kept as simple as possible by reducing them to their simplest **schematic** equivalent. For example, a connection consisting of many kilometres of aerial line, with any number...

...rise to abnormal patterns in the network.

FIG, 6 is a block diagram for the **configuration** toolkit **software** of a preferred embodiment of the second aspect of the invention. The software consists of...

#### Claim

... the predetermined automation scheme adapted to the distribution network configuration.

12 A method for controlling **switchgear** in at least a portion of an electricity distribution network, which network portion includes a...

...sequence prepared for addressing the abnormal operating condition corresponding to said matching pattern.

13 The **switchgear** control method of claim 12 wherein the abnormal condition in step (b) is loss of supply at a predefined place in the network portion.

14 The **switchgear** control method of either claim 12 or 13 wherein the expected abnormal operating patterns include...

...B) an expected tripped pattern; and/or  
(C) an expected source supply pattern.

15 The **switchgear** control method of any one of claims 12 to 14 wherein the control apparatus for...

...network state matches an expected abnormal operating pattern.

Substitute Sheet (Rule 26) RO/AU

. The **switchgear** control method of claim 15 wherein the control apparatus assumes control after a predetermined time delay.

17 The **switchgear** control method of any one of claims 12 to 16 wherein the step of checking...

...control apparatus is already in  
1 0 control of the group of switches.



43 The configuration method of any one of...

...wherein,  
upon analysis of the representation created by the user, further abnormal operating patterns are **automatically produced** and presented to a user.

44 An automated tool for configuration of control apparatus associated...

8/3,K/10 (Item 5 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00557879

**METHOD AND SYSTEM FOR EFFICIENT PACKET DATA TRANSPORT**  
**PROCEDE ET SYSTEME DE TRANSPORT EFFICACE DE DONNEES EN PAQUETS**

Patent Applicant/Assignee:

WONDERWARE CORPORATION,  
WONG Steven,  
KNOBBE Roger,

Inventor(s):

WONG Steven,  
KNOBBE Roger,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200021252 A1 20000413 (WO 0021252)

Application: WO 99US23334 19991006 (PCT/WO US9923334)

Priority Application: US 98103363 19981007; US 99273368 19990322

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB  
GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD  
MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG  
US UZ VN YU ZA ZW GH GM KE LS MW SD SL SZ TZ UG ZW AM AZ BY KG KZ MD RU  
TJ TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG  
CI CM GA GN GW ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 3980

Fulltext Availability:

Detailed Description

Detailed Description

... data) associated with the transport of measurement data to a subscriber.

Another consideration in the **design** of current industrial control systems is the potential for the flow of high volumes of...maximum. queuing time, data transmission from the source node is resumed.

**BRIEF DESCRIPTION OF THE DRAWINGS**

In the accompanying **drawings** .

FIG. 1 is a block diagram of a computer system in which the present invention...

...indicates system operations. Display objects can be pictured on the display and the user can **designate** data operations on the display by using the mouse 1 1 6 or equivalent graphical...

...of the Wonderware Intouch module enables rapid development of three dimensional representations of electrical distribution **switchgear** . The **switchgear** elevational representations have logical connections to the **switchgear** devices. An elevation can be modified to any dimensions with an essentially infinite number of...

...and arrangements of meters and protection devices to quickly and accurately represent a customer's **switchgear** . In addition, a tabular representation of metering and setup/set point information is **generated automatically** with the appropriate database server links established. The accompanying database links in other areas of...

...control.

FIG. 2 is a flowchart representing a message processing subroutine associated with an application **program** , at the workstation 102 **configured** to receive measurement or other data from one or more sensors 120. It is initially...

8/3,K/11 (Item 6 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00553090 \*\*Image available\*\*

**MAN-MACHINE INTERFACE FOR A CUSTOM TABULAR DISPLAY**  
**INTERFACE HOMME-MACHINE POUR AFFICHAGE TABULAIRE PERSONNALISE**

Patent Applicant/Assignee:

GENERAL ELECTRIC COMPANY,

Inventor(s):

THOMAS Robert P,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200016463 A1 20000323 (WO 0016463)

Application: WO 99US21226 19990915 (PCT/WO US9921226)

Priority Application: US 98156167 19980917

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GD GE GH  
GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN  
MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW  
GH GM KE LS MW SD SL SZ TZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH CY  
DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW ML  
MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 5833

Fulltext Availability:

Detailed Description

Detailed Description

... table wizard for displaying the selected parameter in a table.

2

**BRIEF DESCRIPTION OF THE DRAWINGS**

Referring now to the **drawings** wherein like elements are numbered alike in the several FIGURES.

FIGURE 1 is a diagrammatic...

...1 1 is a view of a wizard dialog box window generated by the computer **software** of FIGURE 4 for **configuring** the data displayed in the custom tabular display of a selected device prior to being...

...FIGURE 12 is a view of a wizard dialog box window generated by the computer **software** of FIGURE 4 for **configuring** the data displayed in the custom tabular display; and

FIGURE 13 is a view of a wizard dialog box window generated by the computer **software** of FIGURE 4 for **configuring** the data displayed in the custom tabular display of a selected device after being configured...64 includes a software toolkit for rapid development of three-dimensional representations of electrical distribution **switchgear** 68, as shown in FIGURE 5. These **switchgear** elevations have logical connections to the **switchgear** devices 13. Referring to FIGURE 5, a typical **switchgear** elevation developed with the so-called Power Wizards illustrated in FIGURES 6 - 9 is shown...

...and arrangements of meters and protection devices to quickly and accurately represent a customer's **switchgear** . The Power Wizards eliminate the necessity to draw each individual component line by line. The...

...or dropped onto the appropriate panels in the same locations as the customer's actual **switchgear** . These items have dialog boxes associated with them that are opened by double clicking on...

...10, an example of a Power Wizard of a metering device 28. The Power Wizards **instantly develop** a standard looking interface for a particular device type. These Power Wizards also create from...

?

11/3,K/1 (Item 1 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

07647039 \*\*Image available\*\*  
DEVICE AND METHOD FOR AUTOMATICALLY CREATING USER INTERFACE PROGRAM,  
PROGRAM AND STORAGE MEDIUM

PUB. NO.: 2003-140893 [JP 2003140893 A]  
PUBLISHED: May 16, 2003 (20030516)  
INVENTOR(s): KAWABATA TAICHI  
MINATO KENJI  
TOJO HIROSHI  
APPLICANT(s): NIPPON TELEGR & TELEPH CORP (NTT)  
APPL. NO.: 2001-337715 [JP 2001337715]  
FILED: November 02, 2001 (20011102)

ABSTRACT

...operation object data of UI users obtained from a sequence drawing and a use case **drawing** 90 in the UML **specification** . A GUI **program automatic creating** device 10 uses a GUI template program 30 comprising a template file and a template...

11/3,K/2 (Item 2 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

06988432 \*\*Image available\*\*  
AUTOMATIC GENERATION SUPPORTING SYSTEM FOR LADDER SEQUENCE PROGRAM SPECIFICATION

PUB. NO.: 2001-216007 [JP 2001216007 A]  
PUBLISHED: August 10, 2001 (20010810)  
INVENTOR(s): IMANAGA KENJIRO  
APPLICANT(s): HITACHI ENG CO LTD  
APPL. NO.: 2000-032782 [JP 200032782]  
FILED: February 04, 2000 (20000204)

ABSTRACT

... ladder sequence program specification for improving productivity at the time of preparing a ladder sequence **program specification** from a control **design specification** .

SOLUTION: At the time of **automatically generating** a ladder sequence **program specification** 5, logic circuit information to be converted into a ladder sequence is extracted as extracted...

11/3,K/3 (Item 3 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

04593698 \*\*Image available\*\*  
TEST SPECIFICATION GENERATING SYSTEM

PUB. NO.: 06-265598 [JP 6265598 A]  
PUBLISHED: September 22, 1994 (19940922)  
INVENTOR(s): TOBA TADANOBU  
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP

(Japan)  
APPL. NO.: 05-051655 [JP 9351655]  
FILED: March 12, 1993 (19930312)  
JOURNAL: Section: P, Section No. 1845, Vol. 18, No. 670, Pg. 68,  
December 16, 1994 (19941216)

ABSTRACT

... delivery test of LSI with no input miss or calculation miss while shortening the test **design** time, and to allow **automatic generation** of an optimized test **program** while checking manually prepared test **specifications** .

11/3,K/4 (Item 4 from file: 347)

DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

04542873 \*\*Image available\*\*  
AUTOMATIC PROGRAM GENERATION DEVICE

PUB. NO.: 06-214773 [JP 6214773 A]  
PUBLISHED: August 05, 1994 (19940805)  
INVENTOR(s): TANADA SEIICHI  
APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 05-006571 [JP 936571]  
FILED: January 19, 1993 (19930119)  
JOURNAL: Section: P, Section No. 1823, Vol. 18, No. 584, Pg. 93,  
November 08, 1994 (19941108)

ABSTRACT

...generation time of the hole programs and to improve execution efficiency at the time of **automatically generating the program** by dividing a **program design specification** for respective and optimum functions and automatically generating the programs for the respective function...

11/3,K/5 (Item 5 from file: 347)

DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

04143156 \*\*Image available\*\*  
AUTOMATICALLY PROGRAMMING SYSTEM

PUB. NO.: 05-134856 [JP 5134856 A]  
PUBLISHED: June 01, 1993 (19930601)  
INVENTOR(s): YAMAMOTO HIROSHI  
HASHIMOTO NAOKI  
MORIOKA YOSUKE  
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP  
(Japan)  
HITACHI SOFTWARE ENG CO LTD [472485] (A Japanese Company or Corporation), JP (Japan)  
APPL. NO.: 03-325380 [JP 91325380]  
FILED: November 13, 1991 (19911113)  
JOURNAL: Section: P, Section No. 1614, Vol. 17, No. 515, Pg. 106,  
September 16, 1993 (19930916)

ABSTRACT

... specification 102 and by combining a relevant program part 105 with a

program pattern 104 designated by a program specification 103, a program source 106 is automatically generated .

11/3,K/6 (Item 6 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

03915220 \*\*Image available\*\*  
AUTOMATIC GENERATION DEVICE FOR PROGRAM

PUB. NO.: 04-280320 [JP 4280320 A]  
PUBLISHED: October 06, 1992 (19921006)  
INVENTOR(s): TAKEUCHI HIROSHI  
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 03-069135 [JP 9169135]  
FILED: March 08, 1991 (19910308)  
JOURNAL: Section: P, Section No. 1487, Vol. 17, No. 76, Pg. 153,  
February 16, 1993 (19930216)

#### ABSTRACT

PURPOSE: To prevent the wasteful generation of a pair of a partial specification and design knowledge which makes the specification minute in an automatic program generation device which repeatedly converts the specification of a program through the use of a design knowledge base and which generates the target program...

11/3,K/7 (Item 7 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2005 JPO & JAPIO. All rts. reserv.

02931774 \*\*Image available\*\*  
DESIGN SUPPORTING DEVICE

PUB. NO.: 01-229374 [JP 1229374 A]  
PUBLISHED: September 13, 1989 (19890913)  
INVENTOR(s): YAMADA TAIZO  
TERADA SACHIKO  
APPLICANT(s): MEIDENSHA CORP [000610] (A Japanese Company or Corporation),  
JP (Japan)  
APPL. NO.: 63-055526 [JP 8855526]  
FILED: March 09, 1988 (19880309)  
JOURNAL: Section: P, Section No. 972, Vol. 13, No. 550, Pg. 93,  
December 08, 1989 (19891208)

#### ABSTRACT

... To facilitate design and development by providing an inference mechanism, a knowledge base and a design information file, etc., and generating a desired program automatically from design specifications and a program parts previously prepared through interaction with a designer...

... and simultaneously, stores additional information necessitated for the design. Thus, computerization becomes possible, and the design can be supported by generating the desired program automatically from the design specifications and the previously prepared program part through the interaction with the designer. Namely, since complicated work (module arrangement, allocation of...

11/3,K/8 (Item 1 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2005 Thomson Derwent. All rts. reserv.

012778388 \*\*Image available\*\*  
WPI Acc No: 1999-584614/199950  
XRPX Acc No: N99-432053

**Automatic programming apparatus for compound processing machine - has product configuration unit which configures required product from among those processed by several processing units**

Patent Assignee: MURATA KIKAI KK (MURK )  
Number of Countries: 001 Number of Patents: 002  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 11254268	A	19990921	JP 9857914	A	19980310	199950 B
JP 3269450	B2	20020325	JP 9857914	A	19980310	200222

Priority Applications (No Type Date): JP 9857914 A 19980310

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 11254268	A		7	B23Q-015/00	
JP 3269450	B2		7	B23Q-015/00	Previous Publ. patent JP 11254268

...Abstract (Basic): ADVANTAGE - Since product configuration unit configures a required product, according to the **automatically generated** processed program, configuration accuracy is enhanced.  
DESCRIPTION OF **DRAWING** (S) - The figures shows block diagram of conception components of the automatic programming apparatus and...

11/3,K/9 (Item 2 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2005 Thomson Derwent. All rts. reserv.

012511736 \*\*Image available\*\*  
WPI Acc No: 1999-317842/199927  
XRPX Acc No: N99-238064

**Automatic program specification generation support system for controlling power generating plant, chemical processing plant - includes symbol transducer which performs conversion of symbol of extract information to that of program specification with reference to corresponding surfaces**

Patent Assignee: HITACHI ENG CO LTD (HITJ )  
Number of Countries: 001 Number of Patents: 002  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 11110203	A	19990423	JP 97265896	A	19970930	199927 B
JP 3581538	B2	20041027	JP 97265896	A	19970930	200470

Priority Applications (No Type Date): JP 97265896 A 19970930

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 11110203	A		23	G06F-009/06	
JP 3581538	B2		27	G06F-009/44	Previous Publ. patent JP 11110203

...Abstract (Basic): ADVANTAGE - Improves productivity while producing program specification. DESCRIPTION OF DRAWING(S) - The figure shows **schematic** block diagram of **automatic program specification generation** system. (2) Control design specification; (5) Database; (7) Program specification; (10) Control design specification

production unit; (20) **Design** information addition receiving unit;  
(21a,22a) Software frame data; (30) Information extraction unit; (30a)  
Extract...

**11/3,K/10** (Item 3 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2005 Thomson Derwent. All rts. reserv.

004293560

WPI Acc No: 1985-120438/198520

XRPX Acc No: N85-090503

**Program test scenario generated from specification - creates initial  
program specification used as guide-line for programming**

Patent Assignee: ANONYMOUS (ANON )

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
RD 252001	A	19850410				198520 B

Priority Applications (No Type Date): RD 85252001 A 19850320

...Abstract (Basic): In addition to generating program code, it is also  
necessary to generate the **program** test scenario from the **program  
specifications** . As with the generation of **program** code, this  
**specification** source uses templates **designed** to **produce** the  
program test scenario. **Automatic generation** provides a more  
complete scenario which is updated as specifications change...

**11/3,K/11** (Item 1 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2005 European Patent Office. All rts. reserv.

00553452

**System design method.**

**Systementwurfsverfahren.**

**Procede de conception de systeme.**

PATENT ASSIGNEE:

TELEVERKET, (639890), , S-123 86 Farsta, (SE), (applicant designated  
states: BE;DE;ES;FR)

INVENTOR:

Peterson, Mats, Telia Research AB, Box 85, S-201 20 Malmo, (SE)

Hedman, Leif, Telia Research AB, Box 85, S-20120 Malmo, (SE)

LEGAL REPRESENTATIVE:

Karlsson, Berne et al (23277), Telia Research AB Kgp (Patent Department)

Vitsandsgatan 9, 123 86 Farsta, (SE)

PATENT (CC, No, Kind, Date): EP 540487 A2 930505 (Basic)

EP 540487 A3 931118

APPLICATION (CC, No, Date): EP 92850245 921013;

PRIORITY (CC, No, Date): SE 913210 911101

DESIGNATED STATES: BE; DE; ES; FR

INTERNATIONAL PATENT CLASS: G06F-009/44;

ABSTRACT WORD COUNT: 162

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
----------------	----------	--------	------------

CLAIMS A	(English)	EPABF1	1827
----------	-----------	--------	------

SPEC A	(English)	EPABF1	8998
--------	-----------	--------	------

Total word count - document A			10825
-------------------------------	--	--	-------



Total word count - document B 0  
Total word count - documents A + B 10825

...SPECIFICATION of powerful operational specification systems which are industrially applicable in the field of user interface **design** .  
Transformational implementation entails the **automatic generation** of a **program** from a **specification** . It is in many ways similar to the waterfall approach in that it starts with...

11/3,K/12 (Item 1 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

01222763

**AUTOMATED SOURCE CODE SOFTWARE PROGRAMMER'S MANUAL GENERATOR**  
**GENERATEUR AUTOMATISE DE MANUEL DE PROGRAMMEUR A PARTIR D'UN LOGICIEL A**  
**CODE SOURCE**

Patent Applicant/Assignee:

ELECTRONIC DATA SYSTEMS CORPORATION, 5400 Legacy Drive,, H3-3A-05, Plano, TX 75024, US, US (Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

KAMALAKANTHA Chandra, 4401 Creekstone Drive, Plano, TX 75093, US, US (Residence), IN (Nationality), (Designated only for: US)

Legal Representative:

MYSLIWIEC Richard (agent), EDS, 5400 Legacy Drive, H3-3A-05, Plano, TX 75024, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200529324 A2 20050331 (WO 0529324)

Application: WO 2004US30425 20040917 (PCT/WO US04030425)

Priority Application: US 2003503557 20030917; US 2004854118 20040526

Designated States:

(All protection types applied unless otherwise stated - for applications 2004+)

AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM  
DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC  
LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO  
RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW  
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PL PT RO  
SE SI SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) BW GH GM KE LS MW MZ NA SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 3683

Fulltext Availability:

Detailed Description

Detailed Description

... and technical design

details in one place (i.e. in the code module itself) and  
**automatically generate** the **program specification** , **design** and  
**program** flow from the code with zero effort during  
compilation (i.e., before the module is...

11/3,K/13 (Item 2 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

01112070      \*\*Image available\*\*

**VERTICAL REQUIREMENTS DEVELOPMENT SYSTEM AND METHOD**  
**SYSTEME ET PROCEDE DE HIERARCHISATION DES EXIGENCES**

Patent Applicant/Assignee:

RAYTHEON COMPANY, 141 Spring Street, Lexington, MA 02421, US, US  
(Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

BROWN Robert S, 3656 West Gailey Drive, Tucson, AZ 85741, US, US  
(Residence), US (Nationality), (Designated only for: US)

Legal Representative:

SARALINO Mark D (agent), Renner, Otto, Boisselle & Sklar, LLP, 1621  
Euclid Avenue, Nineteenth Floor, Cleveland, OH 44115, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200434249 A1 20040422 (WO 0434249)

Application: WO 2003US31668 20031003 (PCT/WO US03031668)

Priority Application: US 2002417462 20021009; US 2003464939 20030423

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ  
EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK  
LR LS LT LU LV MA MD MG MK MN MW MX MZ NI NO NZ OM PG PH PL PT RO RU SC  
SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW  
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE  
SI SK TR  
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 7113

Fulltext Availability:

Detailed Description

Detailed Description

... requirements and allocates the requirements to the lowest levels  
16

in the system to be **designed**. In the **automatic** specification  
**generation**, the element **designers** may **automatically generate** their  
lower level **specifications** in the **program specification** tree. In  
the system level optimization, the element designers may analyze these  
specifications for achievability...

**11/3,K/14      (Item 3 from file: 349)**

DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00803854      \*\*Image available\*\*

**METHOD AND APPARATUS FOR AUTOMATICALLY GENERATING A PHASE LOCK LOOP (PLL)**  
**PROCEDE ET DISPOSITIF PERMETTANT LA CREATION AUTOMATIQUE D'UNE BOUCLE A**  
**PHASE ASSERVIE**

Patent Applicant/Assignee:

PARTHUS TECHNOLOGIES PLC, 32-34 Harcourt Street, Dublin 2, IE, IE  
(Residence), IE (Nationality), (For all designated states except: US  
ZW)

SILICON SYSTEMS (US) INC, Suite 150, 2033 Gateway Place, San Jose, CA  
95110, US, US (Residence), US (Nationality), (Designated only for: ZW)  
Patent Applicant/Inventor:  
HORAN John, 44 Lindville Blackrock Road, Cork, IE, IE (Residence), IE  
(Nationality), (Designated only for: US)  
RYAN John, Sequoia Monastery Road, Rochestown, Cork, IE, IE (Residence),  
IE (Nationality), (Designated only for: US)  
CAHILL Cairan, 9, Muskery Heights Tower, Blarney, Co. Cork, IE, IE  
(Residence), IE (Nationality), (Designated only for: US)  
DUNPHY Steven, Mount Carmel, Corrig, Portarlinton, Co. Laois, IE, IE  
(Residence), IE (Nationality), (Designated only for: US)  
SMYTH Mark, Lisselan, Tramore, Co. Waterford, IE, IE (Residence), IE  
(Nationality), (Designated only for: US)  
HEARNE Kay, 21 Firgrove Gardens, Bishoptown, Cork, IE, IE (Residence), IE  
(Nationality), (Designated only for: US)  
DONOVAN Niall, Sunnyhill Leap, Co. Cork, IE, IE (Residence), IE  
(Nationality), (Designated only for: US)  
KIELY Tholom, 70 Pinewood Elm Park, Wilton, Cork, IE, IE (Residence), IE  
(Nationality), (Designated only for: US)  
Legal Representative:  
MALLIE Michael J (agent), Blakely, Sokoloff, Taylor & Zafman LLP, 7th  
floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025, US,  
Patent and Priority Information (Country, Number, Date):  
Patent: WO 200137429 A1 20010525 (WO 0137429)  
Application: WO 2000US31778 20001116 (PCT/WO US0031778)  
Priority Application: US 99166096 19991117; US 2000707330 20001106  
Designated States:  
(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)  
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE  
ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT  
LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT TZ UA UG US UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM  
Publication Language: English  
Filing Language: English  
Fulltext Word Count: 15633

Fulltext Availability:  
Detailed Description

Detailed Description  
... configurable block.

Generally, configurable aspects of the layout sequence are coextensive  
with the specific circuit **design** items that the **automatic PLL  
generation software** has determined beforehand. Thus, **configurable**  
blocks tend to correspond to those  
components that the automatic PLL generation routine played a...

11/3,K/15 (Item 4 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00428792 \*\*Image available\*\*

**METHOD AND SYSTEM FOR CONFIGURING AN ARRAY OF LOGIC DEVICES**  
**PROCEDE ET SYSTEME PERMETTANT DE CONFIGURER UN RESEAU DE DISPOSITIFS**

Sylvia Keys

11-May-05 04:01 PM

## LOGIQUES

Patent Applicant/Assignee:

ATMEL CORPORATION,

Inventor(s):

MASON Martin T,

EVANS Scott C,

ARANAKE Sandeep S,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9819256 A1 19980507

Application: WO 97US18363 19971015 (PCT/WO US9718363)

Priority Application: US 96739606 19961030

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

CN DE GB JP KR AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Publication Language: English

Fulltext Word Count: 8016

Fulltext Availability:

Detailed Description

Detailed Description

... approach is that the designer does not need to determine a priori all possible alternative **designs** and their corresponding **configuration** bitstreams, Rather, the application **software** can determine **design** changes, **on -the- fly** , and **create** a partial configuration bitstream to implement those changes in response to conditions in the environment...

11/3,K/16 (Item 5 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

(c) 2005 WIPO/Univentio. All rts. reserv.

00153148 \*\*Image available\*\*

**METHOD AND SYSTEM FOR CONFIGURING, AUTOMATING AND CONTROLLING OPERATIONS PERFORMED ON PCBS**

**PROCEDE ET SYSTEME PERMETTANT DE CONFIGURER, D'AUTOMATISER ET DE COMMANDER DES OPERATIONS EFFECTUEES SUR CARTES DE CIRCUITS IMPRIMES**

Patent Applicant/Assignee:

CIMM INC,

Inventor(s):

MATRONE John L,

STAMP Ronald L,

BABCOCK Douglas M,

Patent and Priority Information (Country, Number, Date):

Patent: WO 8810060 A1 19881215

Application: WO 88US1820 19880531 (PCT/WO US8801820)

Priority Application: US 87509 19870605

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AT BE CH DE FR GB IT JP KR LU NL SE

Publication Language: English

Fulltext Word Count: 18532

Fulltext Availability:

Detailed Description

Detailed Description

... programming capability which

uses interactive graphics to lay out a configuration of the system and **automatically generates** the system interconnections and control **programs** needed to operate the so **configured** system.

Description of the Accompanying **Drawings**

The present invention will be further described hereinafter with reference to the accompanying drawings wherein...

13/3,K/1 (Item 1 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2005 Thomson Derwent. All rts. reserv.

014836180 \*\*Image available\*\*  
WPI Acc No: 2002-656886/200270  
XRPX Acc No: N02-519342

Parallel switch gear system customization/specification through internet,  
involves retrieving information about switch gear system from database,  
using selected specifications , by accessing products configuring  
software

Patent Assignee: GENERAL ELECTRIC CO (GENE ); LAUFENBERG R (LAUF-I);  
LESLIE D (LESL-I)

Inventor: LAUFENBERG R ; LESLIE D

Number of Countries: 004 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020107749	A1	20020808	US 2001777057	A	20010205	200270 B
CN 1369819	A	20020918	CN 2002103435	A	20020205	200303
KR 2002065371	A	20020813	KR 20026251	A	20020204	200309
TW 571211	A	20040111	TW 2002101047	A	20020123	200442

Priority Applications (No Type Date): US 2001777057 A 20010205

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20020107749	A1	15	G06F-017/60	
CN 1369819	A		G06F-015/16	
KR 2002065371	A		G06F-017/60	
TW 571211	A		G06F-017/60	

... customization/specification through internet, involves retrieving  
information about switch gear system from database, using selected  
specifications , by accessing products configuring software

Inventor: LAUFENBERG R ...

... LESLIE D

Abstract (Basic):

... A product configuration software residing in a server is  
accessed by a user through internet. The switch gear product...  
... 3) Computer-readable medium storing parallel switch gear system  
configuring program ; and...

...4) Parallel switch gear system configuring program .

13/3,K/2 (Item 2 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2005 Thomson Derwent. All rts. reserv.

014384767 \*\*Image available\*\*  
WPI Acc No: 2002-205470/200226  
XRPX Acc No: N02-156446

Controlling and monitoring operation of systems, using particularly  
microprocessor-based controllers and monitoring systems, such as for  
engine generator sets used to provide on-site alternate source of  
electrical energy

Patent Assignee: GENERAL ELECTRIC CO (GENE )

Inventor: LESLIE D S

Number of Countries: 006 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200139352	A1	20010531	WO 2000US30601	A	20001107	200226 B
KR 2001101679	A	20011114	KR 2001709346	A	20010725	200230
CN 1338140	A	20020227	CN 2000803143	A	20001107	200234
US 6668629	B1	20031230	US 99167602	P	19991126	200402
			US 2000629516	A	20000801	
TW 563283	A	20031121	TW 2000124975	A	20001124	200429

Priority Applications (No Type Date): US 2000629516 A 20000801; US 99167602 P 19991126

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
WO 200139352	A1	E 25	H02J-009/06	
Designated States (National): CN IN KR SG				
KR 2001101679	A		F02D-029/06	
CN 1338140	A		H02J-009/06	
US 6668629	B1		G01L-003/26	Provisional application US 99167602
TW 563283	A		H02J-009/06	

Inventor: **LESLIE D S**

Abstract (Basic):

... 204) using protocols of network. Engine-generator sets (210) are controlled by controllers (208), each **configured** with engine-generator control **program**. AN INDEPENDENT CLAIM is made for a controller configured to control and monitor operation of...

**13/3,K/3 (Item 1 from file: 349)**

DIALOG(R)File 349:PCT FULLTEXT

(c) 2005 WIPO/Univentio. All rts. reserv.

00999936 \*\*Image available\*\*

**SYSTEM AND METHOD FOR IDENTIFYING INDIVIDUALS HAVING A DESIRED SKILL SET**  
**SYSTEME ET PROCEDE PERMETTANT D'IDENTIFIER DES INDIVIDUS PRESENTANT UN**  
**ENSEMBLE DE QUALIFICATIONS SOUHAITEES**

Patent Applicant/Assignee:

INTERSECT SOFTWARE CORPORATION, 45975 Nokes Boulevard, Suite 180,  
Sterling, VA 20166, US, US (Residence), US (Nationality)

Inventor(s):

GORUR Ravi Srinath, 13621 Weinstein Court, Centerville, VA 20120, US,  
**LESLIE David**, 800 Lake Windermere Court, Great Falls, VA 22066, US,  
SILTON Roderick Peter, 2655 Fanieul Hall Court, Herndon, VA 20171, US,  
YOST Michael Baker, 1010 Cup Leaf Holly Court, Great Falls, VA 22066, US

Legal Representative:

COOLEY GODWARD LLP (agent), Attn: Patent Group, One Freedom Square,  
Reston Town Center, 11951 Freedom Drive, Reston, VA 20190-5656, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200329919 A2-A3 20030410 (WO 0329919)

Application: WO 2002US30732 20020927 (PCT/WO US02030732)

Priority Application: US 2001325195 20010928; US 2001325218 20010928; US  
2001325194 20010928; US 200273151 20020213; US 200273142 20020213; US  
200273141 20020213

Designated States:

(Protection type is "patent" unless otherwise stated - for applications prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ  
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI

Sylvia Keys

11-May-05 04:02 PM

SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW  
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SK TR  
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English  
Filing Language: English  
Fulltext Word Count: 7038

Inventor(s):  
... **LESLIE David**  
Fulltext Availability:  
Claims

Claim  
... created  
list of individuals to an originator of said request; and  
a computer-usable medium **configured** to store the computer-readable  
**program** codes.

41 A computer program product, comprising:  
22  
computer-readable program code for causing a said user in said work; and  
a computer-usable medium **configured** to store the computer-readable  
**program** codes.

I 0 42. A computer program product, comprising:  
computer-readable program code for causing search of said activity  
database; and  
a computer-usable medium **configured** to store the computer-readable  
**program** codes.

43 An individual identification system, comprising:  
an activity database, said activity database being configured...

13/3,K/4 (Item 2 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
(c) 2005 WIPO/Univentio. All rts. reserv.

00805724 \*\*Image available\*\*  
**METHODS AND APPARATUS FOR WEB-ENABLED ENGINE-GENERATOR SYSTEMS**  
**PROCEDES ET DISPOSITIF DESTINES A DES SYSTEMES MOTEUR-GENERATEUR**  
**ACCESSIBLES VIA INTERNET**

Patent Applicant/Assignee:  
GENERAL ELECTRIC COMPANY, 1 River Road, Schenectady, NY 12345, US, US  
(Residence), US (Nationality)

Inventor(s):  
**LESLIE David Stuart** , 705 Wakeman Avenue, Wheaton, IL 60187, US

Legal Representative:  
MITCHELL James W (et al) (agent), General Electric Company, 3135 Easton  
Turnpike W3C, Fairfield, CT 06431, US,

Patent and Priority Information (Country, Number, Date):  
Patent: WO 200139352 A1 20010531 (WO 0139352)  
Application: WO 2000US30601 20001107 (PCT/WO US0030601)  
Priority Application: US 99167602 19991126; US 2000629516 20000801

Designated States:  
(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

CN IN KR SG  
Publication Language: English



Filing Language: English  
Fulltext Word Count: 6073

Inventor(s):

**LESLIE David Stuart ...**

Fulltext Availability:  
Detailed Description  
Claims

Detailed Description

... tant, a web-based phone or other web-based connectable equipment. Each controller 208 is **configured** with an engine generator control **program**, an embedded web server an I associated web pages and further includes a database 212...

...a user to prepare reports and conduct data analysis. Client-server system 222 is also **configurable** with an E-business enabling **software** agent capable of ordering consumables, spare parts, and services for engine-generators 232 from other...

Claim

... comprising:

at least one clieni system (204) comprising a browser;  
at least one controller (208) **configured** with a control **program**; and  
a network (226) connecting said client system and said controller.

44 A control system...

...generator (2 1 0), said system comprising:

a server (222);  
at least one controller (208) **configured** with a control **program**; and  
a network (226) connecting said server and said controller.

54 A control system (200...

EXAMINER'S  
SEARCH NOTES

See *★* id  
articles herein  
ag 7 11/10/05

File 2:INSPEC 1969-2005/Apr W4  
(c) 2005 Institution of Electrical Engineers  
File 6:NTIS 1964-2005/May W1  
(c) 2005 NTIS, Intl Cpyrght All Rights Res  
File 8:Ei Compendex(R) 1970-2005/May W1  
(c) 2005 Elsevier Eng. Info. Inc.  
File 9:Business & Industry(R) Jul/1994-2005/May 11  
(c) 2005 The Gale Group  
File 13:BAMP 2005/May W1  
(c) 2005 The Gale Group  
File 15:ABI/Inform(R) 1971-2005/May 12  
(c) 2005 ProQuest Info&Learning  
File 16:Gale Group PROMT(R) 1990-2005/May 11  
(c) 2005 The Gale Group  
File 18:Gale Group F&S Index(R) 1988-2005/May 12  
(c) 2005 The Gale Group  
File 20:Dialog Global Reporter 1997-2005/May 12  
(c) 2005 The Dialog Corp.  
File 31:World Surface Coatings Abs 1976-2005/Apr  
(c) 2005 PRA Coat. Tech. Cen.  
File 34:SciSearch(R) Cited Ref Sci 1990-2005/May W2  
(c) 2005 Inst for Sci Info  
File 36:MetalBase 1965-20050509  
(c) 2005 The Dialog Corporation  
File 47:Gale Group Magazine DB(TM) 1959-2005/May 12  
(c) 2005 The Gale group  
File 65:Inside Conferences 1993-2005/May W2  
(c) 2005 BLDSC all rts. reserv.  
File 66:GPO Mon. Cat. 1978-2005/Jun  
(c) format only 2005 The Dialog Corp  
File 80:TGG Aerospace/Def.Mkts(R) 1982-2005/May 12  
(c) 2005 The Gale Group  
File 88:Gale Group Business A.R.T.S. 1976-2005/May 11  
(c) 2005 The Gale Group  
File 92:IHS Intl.Stds.& Specs. 1999/Nov  
(c) 1999 Information Handling Services  
File 94:JICST-EPlus 1985-2005/Mar W3  
(c) 2005 Japan Science and Tech Corp(JST)  
File 95:TEME-Technology & Management 1989-2005/Apr W1  
(c) 2005 FIZ TECHNIK  
File 96:FLUIDEX 1972-2005/Apr  
(c) 2005 Elsevier Science Ltd.  
File 99:Wilson Appl. Sci & Tech Abs 1983-2005/Apr  
(c) 2005 The HW Wilson Co.  
File 103:Energy SciTec 1974-2005/Apr B2  
(c) 2005 Contains copyrighted material  
File 112:UBM Industry News 1998-2004/Jan 27  
(c) 2004 United Business Media  
File 118:ICONDA-Intl Construction 1976-2005/Mar  
(c) 2005 Fraunhofer-IRB  
File 120:U.S. Copyrights 1978-2005/May 03  
(c) format only 2005 The Dialog Corp.  
File 132:S&P's Daily News 1985-2005/May 11  
(c) 2005 McGraw-Hill Companies Inc  
File 144:Pascal 1973-2005/May W1  
(c) 2005 INIST/CNRS  
File 148:Gale Group Trade & Industry DB 1976-2005/May 12  
(c) 2005 The Gale Group  
File 160:Gale Group PROMT(R) 1972-1989  
(c) 1999 The Gale Group  
File 180:Federal Register 1985-2005/May 12

(c) 2005 format only The DIALOG Corp  
 File 194:FBODaily 1982/Dec-2005/Feb  
 (c) format only 2005 The Dialog Corp.  
 File 211:Gale Group Newsearch(TM) 2005/May 12  
 (c) 2005 The Gale Group  
 File 213:ONTAP(R) INSPEC  
 (c) 1989 Institution of Electrical Engineers  
 File 241:Elec. Power DB 1972-1999Jan  
 (c) 1999 Electric Power Research Inst.Inc  
 File 248:PIRA 1975-2005/Apr W4  
 (c) 2005 Pira International  
 File 315:ChemEng & Biotec Abs 1970-2005/Apr  
 (c) 2005 DECHEMA  
 File 323:RAPRA Rubber & Plastics 1972-2005/Apr  
 (c) 2005 RAPRA Technology Ltd  
 File 324:German Patents Fulltext 1967-200518  
 (c) 2005 Univentio  
 File 331:Derwent WPI First View UD=200529  
 (c) 2005 Thomson Derwent  
 File 340:CLAIMS(R)/US Patent 1950-05/May 10  
 (c) 2005 IFI/CLAIMS(R)  
 File 342:Derwent Patents Citation Indx 1978-05/200524  
 (c) 2005 Thomson Derwent  
 File 345:Inpadoc/Fam. & Legal Stat 1968-2004/UD=200518  
 (c) 2005 EPO  
 File 347:JAPIO Nov 1976-2005/Jan(Updated 050506)  
 (c) 2005 JPO & JAPIO  
 File 348:EUROPEAN PATENTS 1978-2005/May W01  
 (c) 2005 European Patent Office  
 File 349:PCT FULLTEXT 1979-2005/UB=20050505,UT=20050428  
 (c) 2005 WIPO/Univentio  
 File 351:Derwent WPI 1963-2005/UD,UM &UP=200529  
 (c) 2005 Thomson Derwent  
 File 388:PEDS: Defense Program Summaries 1999/May  
 (c) 2005 Forecast Intl/DMS  
 File 420:UnCover 1988-2001/May 31  
 (c) 2001 The UnCover Company  
 File 422:REMARC 1900-39 1986/Jul  
 (c) 1986 ISM Library Information Services Ltd  
 File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec  
 (c) 1998 Inst for Sci Info  
 File 440:Current Contents Search(R) 1990-2005/May 12  
 (c) 2005 Inst for Sci Info  
 File 476:Financial Times Fulltext 1982-2005/May 12  
 (c) 2005 Financial Times Ltd  
 File 484:Periodical Abs Plustext 1986-2005/May W2  
 (c) 2005 ProQuest  
 File 505:Asian Co. Profiles 2005/May  
 (c) 2005 FBR Bus Info Svcs  
 File 541:SEC Online(TM) Annual Repts 1997/Sep W3  
 (c) 1987-1997 SEC Online Inc.  
 File 545:Investext(R) 1982-2005/May 11  
 (c) 2005 Thomson Financial Networks  
 File 553:Wilson Bus. Abs. FullText 1982-2004/Dec  
 (c) 2005 The HW Wilson Co  
 File 610:Business Wire 1999-2005/May 12  
 (c) 2005 Business Wire.  
 File 613:PR Newswire 1999-2005/May 12  
 (c) 2005 PR Newswire Association Inc

Set      Items      Description

S1 688 (SWITCHGEAR? OR SWITCH()GEAR?) (5N) (CONFIG? OR SPECIFICATIO-  
 N? OR SPEC OR SPECS)  
 S2 254212 (AUTOMATIC? OR SIMULTAN? OR INSTANT? OR IMMEDIAT? OR INSTA-  
 NTAN? OR ON(1W)FLY) (5N) (DRAWING? OR SCHEMATIC? OR DESIGN?)  
 S3 0 S1(5N)S2  
 S4 10 S1 AND S2  
 S5 7 RD (unique items)  
 S6 18366 S2(5N) (CREAT? OR GENERAT? OR PRODUCING OR PRODUCE? OR DEVE-  
 LOP? ?)  
 S7 6 S6 AND S1  
 S8 3 S7 NOT S5  
 S9 2 RD (unique items)  
 S10 58 S1(5N) (DRAWING? OR SCHEMATIC? OR DESIGN?)  
 S11 8 S10 AND(AUTOMATIC? OR SIMULTAN? OR INSTANT? OR IMMEDIAT? OR  
 INSTANTAN? OR ON(1W)FLY)  
 S12 6 RD (unique items)

*Considered GJ 11/10/05*

5/3,K/1 (Item 1 from file: 15)

DIALOG(R)File 15:ABI/Inform(R)

(c) 2005 ProQuest Info&Learning. All rts. reserv.

02672724 447738141

**Putting the pieces together**

Presti, Joseph A

Transmission & Distribution World v55n10 PP: 26-30 Oct 2003

ISSN: 1087-0849 JRNL CODE: TMD

WORD COUNT: 2078

...TEXT: that pole. Other examples of attributes the company collects are normally open/close on switches, **switchgear configurations**, transformer phase connections and gas pipe tap locations.

GIS is also used as a basis...

...that point, ERP has the in-service date of those assets, based on the original **design**, installed **immediately** upon the crew reporting the job complete. During the closing process of each job, the...

5/3,K/2 (Item 1 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)

(c) 2005 The Gale Group. All rts. reserv.

06689651 Supplier Number: 55974372 (USE FORMAT 7 FOR FULLTEXT)

**Autodesk Unveils AutoCAD OEM 2000 to Its Software Licensing Program.**

PR Newswire, p8864

Oct 4, 1999

Language: English Record Type: Fulltext

Document Type: Newswire; Trade

Word Count: 1113

... markets emerging this year for AutoCAD OEM include cargo management, electronic panel layout, industrial kitchen **configuration**, low-voltage **switch gear** design, network mapping, prefabricated wood and metal buildings design, and transformer and telecommunications switch station...

...for

locating and reusing design data, the Multiple Design Environment for operating between several windows **simultaneously**, and 3D graphics for

sophisticated **design** creation. The Object Properties window is available for users to easily edit intelligent object properties...

5/3,K/3 (Item 2 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)

(c) 2005 The Gale Group. All rts. reserv.

05747111 Supplier Number: 50230415 (USE FORMAT 7 FOR FULLTEXT)

**Switchgear for protection and distribution**

Booth, Malcolm

European Power News, p13

July, 1998

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Tabloid; Trade

Word Count: 1271

... by the size and distribution of loads and the parameters of interconnected networks, the detailed **specification** of the **switchgear** depends on the security of supply and level of automation required.

Increasingly the demand is...

...Uninterruptible power supplies as such are outside the scope of this article but, detailed switchgear **design** can take account of **automatic** supply changeover, minimal maintenance, the connection, maintenance or replacement of switching units without shutdown, the...


5/3,K/4 (Item 3 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

03466668 Supplier Number: 44840005 (USE FORMAT 7 FOR FULLTEXT)  
**HAWKER-SIDDELY CONTRACTS FOR COMPUTERVISION SOFTWARE; COMPUTERVISION WINS  
\$165,000 MEDUSA SOFTWARE AND SERVICES CONTRACT**

PR Newswire, pN/A  
July 14, 1994  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 412

... capabilities, Hawker Siddely found that they could use information captured during the proposal process to **automatically** generate subsequent **drawing** and documentation sets. As a result, Hawker Siddely will call on Computervision for help with...

...was a major reason for the contract award.

Nigel Whittingham, CAE manager for Hawker Siddely Switchgear,   
said "Our products are highly **configured** in order to meet our customers' needs. MEDUSA will help us substantially by automating the..."

5/3,K/5 (Item 1 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2005 European Patent Office. All rts. reserv.

01782062  
**Computer-aided tendering of power supply facilities**  
**Rechnergestutztes Anbieten von Kraftwerksanlagen**  
**Soumission assistee par ordinateur pour installations d'alimentation  
electrique**

PATENT ASSIGNEE:

ABB Technology AG, (2997151), Affolternstrasse 44, 8050 Zurich, (CH),  
(Applicant designated States: all)

INVENTOR:

Bosshart, Peter, Haldengutweg 11, 5610 Wohlen, (CH)  
Steiger, Martin, Wannenstrasse 42, 8610 Uster, (CH)  
Kieboom, Gerardus, Bodenackerstrasse 20, 5417 Untersiggenthal, (CH)  
Buri, Bruno, Bachstrasse 89, 5436 Wurenlos, (CH)  
Landert, Hans-Peter, Zelgstrasse 9, 8610 Uster, (CH)

LEGAL REPRESENTATIVE:

ABB Patent Attorneys (101545), c/o ABB Schweiz AG, Intellectual Property  
(CH-LC/IP), Brown Boveri Strasse 6, 5400 Baden, (CH)  
PATENT (CC, No, Kind, Date): EP 1455286 A1 040908 (Basic)  
EP 1455286 A1 040908

APPLICATION (CC, No, Date): EP 2003405152 030305;  
DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR;  
HU; IE; IT; LI; LU; MC; NL; PT; SE; SI; SK; TR  
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO  
INTERNATIONAL PATENT CLASS: G06F-017/50  
ABSTRACT WORD COUNT: 182  
NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200437	1045
SPEC A	(English)	200437	4340
Total word count - document A			5385
Total word count - document B			0
Total word count - documents A + B			5385

...SPECIFICATION and design options.

The operation of the project planning system comprises: parametrisation of substation information, **automatic design** of the substation control system, **automatic** generation of substation diagrams, determination of operating conditions for substation switchboard sections, automatic determination of...

...to a computer-integrated manufacturing line.

The above state of the art is focused on **automatic design** tools for configuring power supply facilities or components. Such design tools comprise module libraries that...

...are compared to and implemented in a components specification database 23 and, in particular, in **switchgear** data component **specifications** 23a. In order to be accessible from anywhere, in particular, via internet or intranet, the...

...selection of modules, adaptation of modules

- 21 instantiation of modules
- 22 customer requirements
- 23 components **specification** database
- 23a **switchgear** data component **specifications**
- 24 implementation of library, FTP-server
- 25 project folder structure
- 25a project name
- 26a sketch...

5/3,K/6 (Item 2 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01715964

**Gas insulated switchgear**

**Gasisolierte Schaltanlage**

**Appareillage de commutation a isolement gazeux**

PATENT ASSIGNEE:

Hitachi, Ltd., (204145), 6 Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo  
101-8010, (JP), (Applicant designated States: all)

INVENTOR:

Kawamoto, Hideo, c/o Hitachi, Ltd., 5-1, Marunouchi 1-chome, Chiyoda-ku,  
Tokyo 100-8220, (JP)

Yaginuma, Noriyuki, c/o Hitachi, Ltd., 5-1, Marunouchi 1-chome,  
Chiyoda-ku, Tokyo 100-8220, (JP)  
Okubo, Kenichi, c/o Hitachi, Ltd., 5-1, Marunouchi 1-chome, Chiyoda-ku,  
Tokyo 100-8220, (JP)  
Ishiguro, Tetsu, c/o Hitachi, Ltd., 5-1, Marunouchi 1-chome, Chiyoda-ku,  
Tokyo 100-8220, (JP)  
Yaegashi, Masahiro, c/o Hitachi, Ltd., 5-1, Marunouchi 1-chome,  
Chiyoda-ku, Tokyo 100-8220, (JP)

LEGAL REPRESENTATIVE:

Strehl Schubel-Hopf & Partner (100941), Maximilianstrasse 54, 80538  
Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 1406278 A2 040407 (Basic)

APPLICATION (CC, No, Date): EP 2003018257 030811;

PRIORITY (CC, No, Date): JP 2002292216 021004

DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR;  
HU; IE; IT; LI; LU; MC; NL; PT; RO; SE; SI; SK; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK

INTERNATIONAL PATENT CLASS: H01H-033/34

ABSTRACT WORD COUNT: 218

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200415	845
SPEC A	(English)	200415	3611
Total word count - document A			4456
Total word count - document B			0
Total word count - documents A + B			4456

...SPECIFICATION length of the hydraulic operation unit and reducing the  
length of the entire gas insulated **switchgear** in a compact  
**configuration** .

The lock mechanism 270 shown in Fig. 3 is hydraulically connected to  
the operation unit...

...portion.

Fig. 7 shows the detailed structure of the lock mechanism. This lock  
mechanism is **designed** to lock the operation **automatically** since the  
movable contactor 106 does not move in the direction of opening when  
hydraulic...

...length of the hydraulic operation unit and reducing the length of the  
entire gas insulated **switchgear** in a compact **configuration** .

(EFFECTS OF THE INVENTION)

In large-sized gas insulated switchgear adopting a linear gas sealing  
...

5/3,K/7 (Item 1 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

(c) 2005 WIPO/Univentio. All rts. reserv.

01077550 \*\*Image available\*\*

MICROTURBINE ENGINE SYSTEM

SYSTEME DE MOTEUR A MICROTURBINE

Patent Applicant/Assignee:

INGERSOLL-RAND ENERGY SYSTEMS CORPORATION, 30 New Hampshire Avenue,  
Portsmouth, NH 03801-2811, US, US (Residence), US (Nationality)

Sylvia Keys

12-May-05 02:37 PM



Inventor(s):

EBRAHIM Mohammed, 44 Osprey Drive, Portsmouth, NH 03801, US,  
SANDOVAL Daniel, 103 Mount Vernon Street, Dover, NH 03820, US,  
OLIVER Sunit, 51 Gonet Drive, Newmarket, NH 03857, US,  
LAKOV German, 164 Vest Way, North Andover, MA 01845, US,

Legal Representative:

JONES Daniel S (agent), Michael Best & Friedrich LLP, 3773 Corporate  
Parkway, Suite 360, Center Valley, PA 18034-8217, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 2003106828 A2-A3 20031224 (WO 03106828)  
Application: WO 2003US19176 20030618 (PCT/WO US03019176)  
Priority Application: US 2002389765 20020618

Designated States:

(Protection type is "patent" unless otherwise stated - for applications  
prior to 2004)

AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ  
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG  
SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW  
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE  
SI SK TR  
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Publication Language: English

Filing Language: English

Fulltext Word Count: 14557

Fulltext Availability:

Detailed Description

Detailed Description

... 1 5 arrangement, and rotating rectifier (brushless) excitation  
systems) with brushless excitation systems being preferred.

**AUTOMATIC VOLTAGE REGULATOR**

Fig. 4 **schematically** illustrates an **automatic** voltage regulator 1 1 5  
controlling the generator exciter 1 1 0. As described above...

...flow from the generator 90 to the utility grid 120. The system control  
module 130 **configures** the switches 160 in the **switch gear** 135 as  
needed for the different modes of operation.

In a stand-alone mode, the...

...engine is started and brought up to speed with no load applied. The SCM  
130 **configures** the **switch gear** 135 so that the utility breakers 160  
are opened and the generator breakers 155 are...

?

9/3,K/1 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

11388951 SUPPLIER NUMBER: 55974372 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Autodesk Unveils AutoCAD OEM 2000 to Its Software Licensing Program.**

PR Newswire, 8864

Oct 4, 1999

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 1169 LINE COUNT: 00107

... markets emerging this year for AutoCAD OEM include cargo management, electronic panel layout, industrial kitchen **configuration**, low-voltage **switch gear** design, network mapping, prefabricated wood and metal buildings design, and transformer and telecommunications switch station...

...for

locating and reusing design data, the Multiple Design Environment for operating between several windows **simultaneously**, and 3D graphics

for

sophisticated **design creation**. The Object Properties window is available for users to easily edit intelligent object properties and

...

9/3,K/2 (Item 2 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2005 The Gale Group. All rts. reserv.

07469735 SUPPLIER NUMBER: 15574148 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**HAWKER-SIDDELY CONTRACTS FOR COMPUTERVISION SOFTWARE; COMPUTERVISION WINS \$165,000 MEDUSA SOFTWARE AND SERVICES CONTRACT**

PR Newswire, p0714NE005

July 14, 1994

LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

WORD COUNT: 458 LINE COUNT: 00042

... capabilities, Hawker Siddely found that they could use information captured during the proposal process to **automatically generate** subsequent **drawing** and documentation sets. As a result, Hawker Siddely will call on Computervision for help with...

...was a major reason for the contract award.

Nigel Whittingham, CAE manager for Hawker Siddely **Switchgear**, said "Our products are highly **configured** in order to meet our customers' needs. MEDUSA will help us substantially by automating the...

?

12/3,K/1 (Item 1 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2005 Institution of Electrical Engineers. All rts. reserv.

02908500 INSPEC Abstract Number: B87043319

**Title:** Compact design and optimum configuration of gas insulated switchgear  
**Author(s):** Amano, N.; Tsubaki, T.; Kikuchi, T.; Hosokawa, M.; Tamura, M.  
**Author Affiliation:** Hitachi Ltd., Kokubu, Japan  
**Conference Title:** Electric Energy Conference 1986. 'Electric Energy Systems - Today and Tomorrow'. Preprints p.189-93  
**Publisher:** Instn. Eng. Australia, Barton, ACT, Australia  
**Publication Date:** 1986 **Country of Publication:** Australia 309 pp.  
**ISBN:** 0 85825 312 7  
**Conference Sponsor:** Coll. Electr. Eng.; Instn. Eng. Australia; IEE; IEEE  
**Conference Date:** 20-22 Oct. 1986 **Conference Location:** Brisbane, Qld., Australia  
**Language:** English  
**Subfile:** B

**Title:** Compact design and optimum configuration of gas insulated switchgear  
...Abstract: to improve the reliability and maintainability, the techniques to detect the abnormal conditions and an automatic monitoring system have also been described.  
...Identifiers: automatic monitoring system

12/3,K/2 (Item 1 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2005 The Gale Group. All rts. reserv.

06689651 Supplier Number: 55974372 (USE FORMAT 7 FOR FULLTEXT)  
**Autodesk Unveils AutoCAD OEM 2000 to Its Software Licensing Program.**  
PR Newswire, p8864  
Oct 4, 1999  
**Language:** English **Record Type:** Fulltext  
**Document Type:** Newswire; Trade  
**Word Count:** 1113

... markets emerging this year for AutoCAD OEM include cargo management, electronic panel layout, industrial kitchen configuration, low-voltage switch gear design, network mapping, prefabricated wood and metal buildings design, and transformer and telecommunications switch station design...

...for  
locating and reusing design data, the Multiple Design Environment for operating between several windows simultaneously, and 3D graphics for  
sophisticated design creation. The Object Properties window is available for users...

...Availability  
The English language version of the AutoCAD OEM 2000 software developer kit is available immediately worldwide direct from Autodesk. The scalable pricing model includes an up-front license fee and...

12/3,K/3 (Item 1 from file: 103)  
DIALOG(R)File 103:Energy SciTec

(c) 2005 Contains copyrighted material. All rts. reserv.

01335978 EDB-84-033654

**Title: Development trends of modern electrical equipment for the coal industry**

Author(s): Parkhomenko, A.I.

Source: Ugol' (USSR) v 3. Coden: UGOLA

Publication Date: Mar 1983

p 32-35

Language: Russian

...Abstract: the EhKV motor) and conveyors (the EhDKOF motor), electrical equipment for 660 V systems (AV **automatic** circuit breakers, PVI magnetic starters, control systems for shearer loaders, heading machines and conveyors), the KRUV-6 new high voltage **switch - gear** . **Design** and **specifications** of the electrical equipment are discussed. (3 refs.) (In Russian)

12/3,K/4 (Item 1 from file: 324)

DIALOG(R)File 324:German Patents Fulltext

(c) 2005 Univentio. All rts. reserv.

0003013865 \*\*Image available\*\*

**Procedure and installation for hook-up error-protected steering of switchgears of a switchgear.**

**Verfahren und Einrichtung zum schaltfehlergeschützten Steuern von Schaltgeräten einer Schaltanlage.**

Patent Applicant/Assignee:

ABB Patent GmbH,

Inventor(s):

Kreuter Konrad,

Patent and Priority Information (Country, Number, Date):

Patent: DE 4230603 C1 19940324

Application: DE 4230603 19920912

Priority Application: DE 4230603 19920912 (DE 4230603)

Publication Language: German

Fulltext Word Count (English): 6802

Fulltext Word Count (German) : 5650

Fulltext Word Count (Both) : 12452

Fulltext Availability:

Description (English machine translation)

Description (English machine translation)

... more near described in the following

on the basis a remark example represented in the **design** . Show:

Fig. 1 **configuration** of a **switchgear** , Fig. 2

arrangement connected by units and control units, by a communication network work.

Fig...

...announced itself actively after the receipt of a switching command, because it would be presently/ **immediately** the only active one thereby, then it may begin the execution of the instruction only...

12/3,K/5 (Item 2 from file: 324)

DIALOG(R)File 324:German Patents Fulltext

(c) 2005 Univentio. All rts. reserv.

0002712107

PROCEDURES AND CIRCUIT ORDER ZUR PROTECTION OF A CIRCUIT WITH LOW VOLT  
HALOGEN LAMPS  
VERFAHREN UND SCHALTUNGSANORDNUNG ZUR SICHERUNG EINES STROMKREISES MIT  
NIEDERVOLT-HALOGENLAMPEN

Patent Applicant/Assignee:

ZIELASKO HORST, 2890 NORDENHAM, DE,, DE

Inventor(s):

ZIELASKO HORST, 2890 NORDENHAM, DE,, DE

Patent and Priority Information (Country, Number, Date):

Patent: DE 3928801 A1 19910307

Application: DE 3928801 19890831

Priority Application: DE 3928801 19890831 (DE 3928801)

Publication Language: German

Fulltext Word Count (English): 2170

Fulltext Word Count (German) : 1680

Fulltext Word Count (Both) : 3850

Fulltext Availability:

Description (English machine translation)

Description (English machine translation)

... with perfect plant, which can be supervised, takes place within the  
given time interval fully **automatically** .

A first further training of the invention results with the  
means of the requirement 4. Into change over switch marked by a  
choice position "maximum" errors are switched off **immediately** by  
short-circuit of energized construction parts of the lamp plant with  
the occurrence and...

...examples of

the invention is represented and in the following is more near  
described in **designs** . Fig show. 1 a switching  
**configuration** for a protection **switchgear** with partial mechanical  
BauelementenFig. 2 a switching configuration for a protection  
switchgear with partial electronic...

...contact member are a

for mechanical switching on and off and with a danger condition  
**automatic** Auschalten of the lamp circle to one of the output terminals  
of a supply transformer...

...supplied, now

large current value of the working current to a comparator 9.  
From this **instant** on the current value of the working current  
and thus the working current are supervised...

12/3,K/6 (Item 1 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2005 European Patent Office. All rts. reserv.

01288757

Switchgear and method of manufacturing thereof

Schalter und dessen Herstellungsverfahren

Interrupteur et son procede de fabrication

PATENT ASSIGNEE:

Kabushiki Kaisha Toshiba, (2732370), 72, Horikawa-cho, Saiwai-ku,

Kawasaki-shi, Kanagawa-ken, (JP), (Applicant designated States: all)

INVENTOR:

Sylvia Keys

12-May-05 03:00 PM

Ito, Yoshihiro, c/o Toshiba Corporation, Intell. Property Div., 1-1-1,  
Shibaura, Minato-ku, Tokyo, (JP)  
Kinoshita, Susumu, c/o Toshiba Corporation, Intell. Property Div., 1-1-1,  
Shibaura, Minato-ku, Tokyo, (JP)  
Makishima, Satoshi, c/o Toshiba Corporation, Intell. Property Div.,  
1-1-1, Shibaura, Minato-ku, Tokyo, (JP)  
Sekiya, Hiroki, c/o Toshiba Corporation, Intell. Property Div., 1-1-1,  
Shibaura, Minato-ku, Tokyo, (JP)  
Miyagawa, Masaru, c/o Toshiba Corporation, Intell. Property Div., 1-1-1,  
Shibaura, Minato-ku, Tokyo, (JP)  
Shimizu, Toshio, c/o Toshiba Corporation, Intell. Property Div., 1-1-1,  
Shibaura, Minato-ku, Tokyo, (JP)

LEGAL REPRESENTATIVE:

Henkel, Feiler, Hanzel (100401), Mohlstrasse 37, 81675 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 1107409 A1 010613 (Basic)

EP 1107409 A8 011114

APPLICATION (CC, No, Date): EP 2000126303 001201;

PRIORITY (CC, No, Date): JP 99342066 991201

DESIGNATED STATES: CH; DE; LI

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H02B-013/035

ABSTRACT WORD COUNT: 72

NOTE:

Figure number on first page: 2

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200124	940
SPEC A	(English)	200124	9250
Total word count - document A			10190
Total word count - document B			0
Total word count - documents A + B			10190

...SPECIFICATION it is possible to dispense with troublesome adjustments after molding, because the dimensions are determined **automatically** . Moreover, a switchgear must perforce have an input member and an output member, and by...and an operational rod 31 and assembled on an operational mechanism not illustrated in the **drawings** to form a **switchgear** .

In the circuit **configuration** illustrated in Fig. 3, a voltage transformer (VT) 25 molded en bloc is attached to...

...it is possible to dispense with troublesome adjustments after molding, because the dimensions are determined **automatically** . Moreover, the switchgear 1 which must perforce have an input member 3 und an output... the present invention with reference to Fig. 19.

The fifth embodiment is a method of **simultaneously** molding the movable side conductors 9 and 15 in the molded switchgear illustrated in Fig...

?